×

Use these options if you get one of the error messages:

No convergence in transient analysis Cannot find DC operating point - . . .

Try Extended precision first as this option has no disadvantages other than a small speed loss

mode (fastest)			
Advanced iteration Extended precision Extended/quad precision			
			e+\
		ou precision (sione:	×1,7
1p	Default		
là	□ Default		
ne +12	urces 🔀 Disable		
fiers			
e 0			
hunt capacitance g	lobally		
	ed precision ed/quad precision ed precision (slowe: 1p or discontinuous so 1e+12 fiers		

Selects the solver iteration precision and mode. Each of these options improves convergence at the expense of simulation speed. These options do not impair the simulation accuracy. If using either quad precision mode, consider reducing the minimum time step.

Click on help (?) for more info

Defaults to 1pA but this is sometimes too low for some circuits. Setting this to a value of up to 1nA often solves convergence problems

Consider reducing this value if using a quad precision iteration mode. Also the error Timestep too small can be resolved by setting this to a lower value. This setting will **not** significantly affect simulation time

Set this to a value not lower than 1E9. Sets a slew rate for arbitrary sources with expressions in the form:

IF(expression, constant, constant)

Such sources are common in power controller models but these are discontinuous and can lead to convergence problems. Applying a slew rate removes the discontinuity and improves convergence

Shunt capacitance is a small capacitance applied to ground from all top level nodes, that is nodes not in a subcircuit or hierarchical block. Even a value as small as 1fF can improve convergence.

If you check **Apply shunt capacitance globally**, the capacitance will be applied to all nodes in the circuit. Must be used with care as even very small capacitances can affect the behaviour of subcircuit models.

Inductor loss TC applies a resistance across all inductors with a value of L/TC. A value as low as 1fs can aid convergence in some circuits.

OK.

el Help

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Tobias Bleicker

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