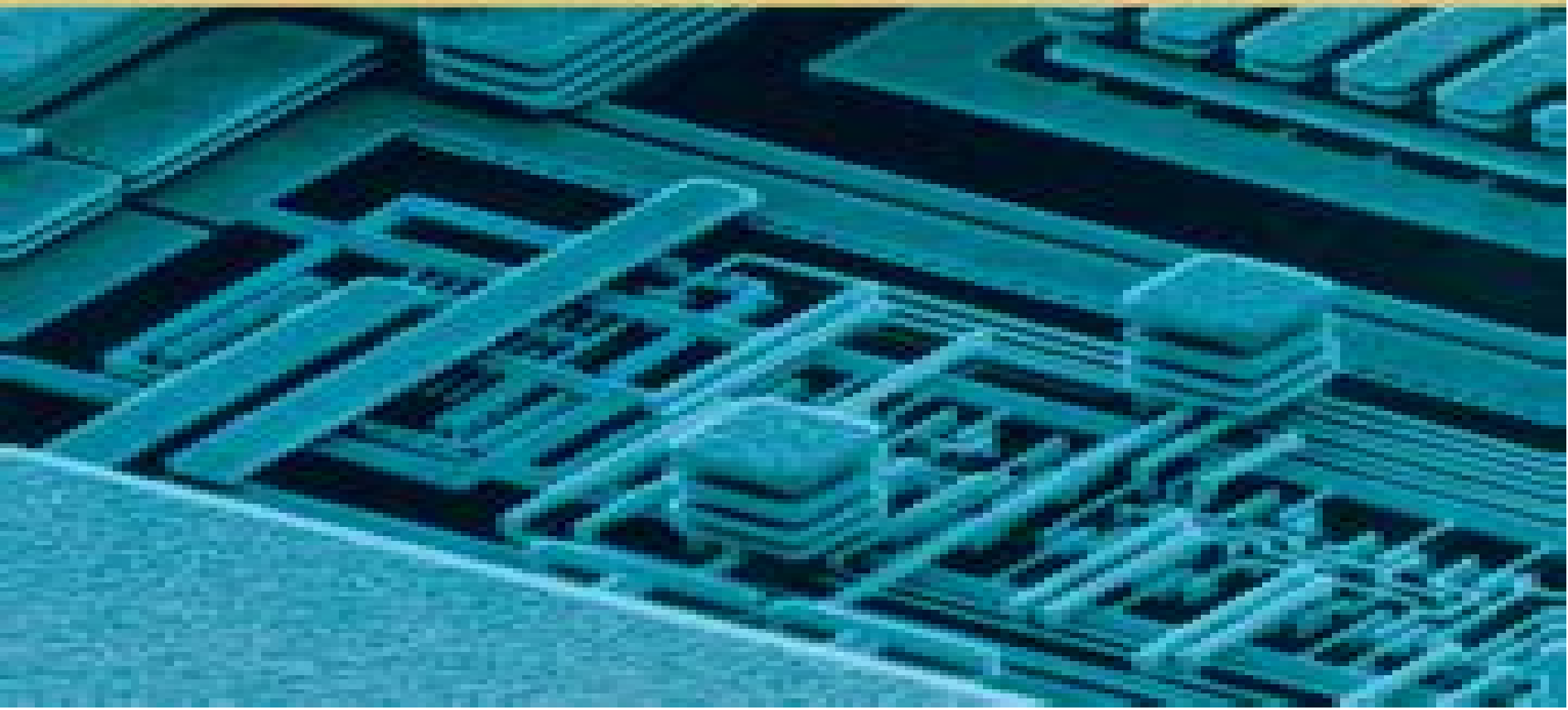


Second Edition

Fundamentals of

Modern VLSI Devices

Yuan Taur and Tak H. Ning



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useful in practical transistor design and in the classroom Every chapter has been updated to include the latest developments such as MOSFET scale length theory high field transport model and SiGe base bipolar devices *Scientific and Technical Aerospace Reports* ,1986 **Subject Guide to Books in Print** ,1983 **Scientific and Technical Books and Serials in Print** ,1989 Books in Print ,1987 **Fundamentals Of Modern Vlsi Devices** Yuan Taur,Tak H. Ning,2004-10-01 This book examines in detail the basic properties and design including chip integration of CMOS and bipolar VLSI devices and discusses the various factors that affect their performance The authors begin with a thorough review of the relevant aspects of semiconductor physics and proceed to a description of the design of CMOS and bipolar devices The optimization of these devices for VLSI applications is also covered The authors highlight the intricate interdependencies and subtle trade offs between those device parameters such as power consumption and packing density that affect circuit performance and manufacturability They also discuss in detail the scaling and physical limits to the scaling of CMOS and bipolar devices The book contains many exercises and can be used as a textbook for senior undergraduate or first year graduate courses on microelectronics or VLSI devices It will also be a valuable reference volume for practising engineers involved in research and development in the electronics industry **Forthcoming Books** Rose Arny,1997-04

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