M.C.A.

Q.P. Code: 587900

(3 Hours)

[Total Marks: 80

	N. B.: (1)	Question No. Solve any thre	1 is compulsory. se from remaining five qu	sections	
	(3)	Figures to the	right indicate full marks	5.	
	(4)	Assume suitab	ale data if necessary and r	nention the same in the answe	rsheet. o
Q1					439
		ntiate between	microprocessor	Imigracontrollar	80 5
b	Differentiate between microprocessor and microcontroller. Explain 8051 assembler directives.				
c					
d			n metrics of an Emb	padded System 9	5 5
		- cripridge design	in medies of all Ellie	bedded System.	
Q2				100	
a		PORT 1 struc	ture of 8051		10
ь				51 microcontroller, 8kB	10
	EPRON	4 & 8kB RAM		- The occupantici, bill	10
Q3				- C	
a	WAP for 8051 microcontroller to generate a square waveform of				10
	frequen	cy 1kHz and 50	0% duty cycle at pir	P1.1. Assume 8051 is	
100	operatir	ig at frequency	12MHz		
ь	Interfac	e 8051 with D	AC 0808, WAP to g	enerate a triangular way	veform. 10
			(C)	- /	
Q4	-	1. 1. 1. 1.			
3.	Draw ar	id explain data	flow model of ARM	M7.	10
ь	Explain	register organi	zation of ARM7.		10
Q5			1.50		
a	Explain	A DAT fallows			
-	Explain ARM following instructions: CMP r0, r1, LSR#7 ADD r2.r1.r0 LDR r10 [r1]				10
	AND rl		ADD r2,r1,r0	LDR r10,[r1]	240
ь			OR r2,r2,#3		
	Explain digital camera as an example of embedded system.				10
Q6	Short No	Mes.			
a		structure of 80	051		Q2020
ь		odes 8051.			10
- 12.00	100	and the second of the second			10

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