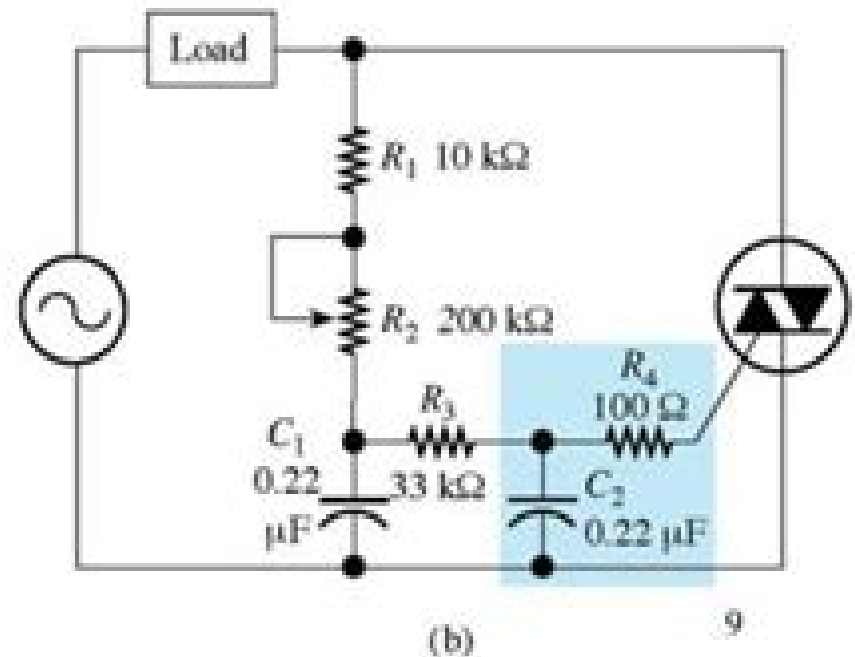
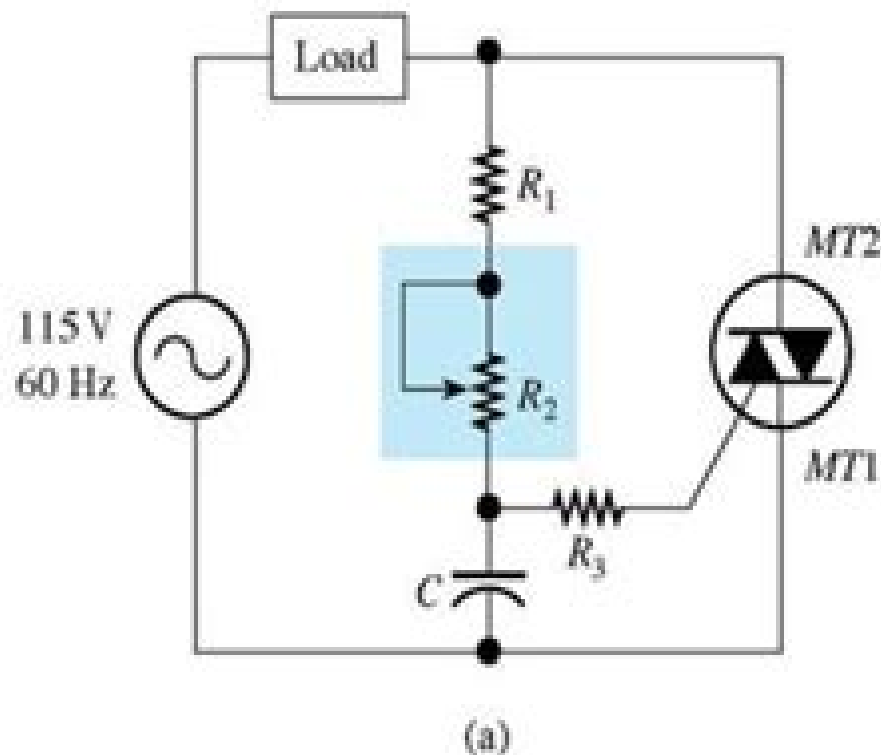


Triggering Methods For Triacs

Figure 6–4

(a) Simple gate control circuit (triggering circuit) for a triac. Firing delay is adjusted by potentiometer R_2 . (b) Improved gate control circuit, which allows a wider range of adjustment of firing delay.



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