Ivan Radojevic · Zoran Salcic

Embedded Systems Design Based on Formal Models of Computation

KCS Murti

Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15 Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts It targets heterogeneous systems by combining finite state machines FSM with synchronous dataflow graphs SDFG FSMs are connected in the same way as in Argos a Statecharts variant with purely synchronous semantics using three operators synchronous parallel refinement and hiding The fourth operator called asynchronous parallel is introduced in DFCharts to connect FSMs with SDFGs In the formal semantics of DFCharts the operation of an SDFG is represented as an FSM Using this representation SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM This allows system properties to be verified globally The practical application of DFCharts has been demonstrated by linking it to widely used system level languages Iava Esterel and SystemC Design Principles for Embedded Systems KCS Murti, 2021-09-20 The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science This book attempts to bridge the gap between electronics and computer science students providing complementary knowledge that is essential for designing an embedded system The book covers key concepts tailored for embedded system design in one place. The topics covered in this book are models and architectures Executable Specific Languages System Unified Modeling Language real time systems real time operating systems networked embedded systems Embedded Processor architectures and platforms that are secured and energy efficient A major segment of embedded systems needs hard real time requirements This textbook includes real time concepts including algorithms and real time operating system standards like POSIX threads Embedded systems are mostly distributed and networked for deterministic responses The book covers how to design networked embedded systems with appropriate protocols for real time requirements Each chapter contains 2 3 solved case studies and 10 real world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs Software Engineering and Formal Methods Alessandro Cimatti, Marjan Sirjani, 2017-08-24 This book constitutes the refereed proceedings of the 15th International Conference on Software Engineering and Formal Methods SEFM 2017 held in Trento Italy in September 2017 The 17 full papers and 6 short papers presented were carefully reviewed and selected from 102 submissions. The papers deal with a large range of topics in the following research areas new frontiers in software architecture software verification and testing software development methods application and technology transfer security and safety and design principles 16th International Conference on Information Technology-New Generations (ITNG 2019) Shahram Latifi, 2019-05-22 This 16th International Conference on Information Technology New Generations ITNG continues an annual event focusing on

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a wealth of diverse applications including those in automotive electronics industrial automated systems and building automation and control Now a new resource is required to report on current developments and provide a technical reference for those looking to move the field forward yet again Divided into two volumes to accommodate this growth the Embedded Systems Handbook Second Edition presents a comprehensive view on this area of computer engineering with a currently appropriate emphasis on developments in networking and applications Those experts directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews that explore cutting edge developments and deployments and identify potential trends This first self contained volume of the handbook Embedded Systems Design and Verification is divided into three sections It begins with a brief introduction to embedded systems design and verification It then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Those interested in taking their work with embedded systems to the network level should complete their study with the second volume Network **Embedded Systems** Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10 Michael Hübner,2010 Languages, Design Methods, and Tools for Electronic System **Design** Frank Oppenheimer, Julio Luis Medina Pasaje, 2015-12-11 This book brings together a selection of the best papers from the seventeenth edition of the Forum on specification and Design Languages Conference FDL which took place on October 14 16 2014 in Munich Germany FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modeling and verification of integrated circuits complex hardware software embedded systems and mixed technology EDA for IC System Design, Verification, and Testing Louis Scheffer, Luciano Lavagno, Grant systems Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set

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