

Embedded Systems

Ian O'Connor
Gabriela Nicolescu *Editors*

Integrated Optical Interconnect Architectures for Embedded Systems

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Integrated Optical Interconnect Architectures for Embedded Systems Ian O'Connor, Gabriela

Nicolescu, 2012-11-07 This book provides a broad overview of current research in optical interconnect technologies and architectures. Introductory chapters on high performance computing and the associated issues in conventional interconnect architectures and on the fundamental building blocks for integrated optical interconnect provide the foundations for the bulk of the book which brings together leading experts in the field of optical interconnect architectures for data communication. Particular emphasis is given to the ways in which the photonic components are assembled into architectures to address the needs of data intensive on chip communication and to the performance evaluation of such architectures for specific applications.

Parallel Architectures, Algorithms and Programming Hong Shen, Yingpeng Sang, 2020-01-25 This book constitutes the refereed proceedings of the 10th International Symposium on Parallel Architectures, Algorithms and Programming PAAP 2019 held in Guangzhou, China, in December 2019. The 39 revised full papers and 8 revised short papers presented were carefully reviewed and selected from 121 submissions. The papers deal with research results and development activities in all aspects of parallel architectures, algorithms and programming techniques.

Advances in Dependability Engineering of Complex Systems Wojciech Zamojski, Jacek Mazurkiewicz, Jarosław Sugier, Tomasz Walkowiak, Janusz Kacprzyk, 2017-05-29 This book gathers the proceedings of the 2017 DepCoS RELCOMEX, an annual conference series that has been organized by the Department of Computer Engineering at the Faculty of Electronics, Wrocław University of Science and Technology since 2006. Its mission is to continue the heritage of the other two cycles of events: the RELCOMEX conferences (1977-89) and Microcomputer Schools (1985-95), so this year we can celebrate the 40th anniversary of its origins. In contrast to those preceding series which were focused on conventional reliability analysis, the goal of DepCoS is to promote a more comprehensive approach to system performability, which is now commonly called dependability. This innovative research area provides answers to the latest challenges in reliability evaluation for contemporary complex systems. Its novelty is based on a multi-disciplinary approach to system theory, technology and maintenance of systems operating in real environments. Dependability analyses concentrate on the efficient completion of tasks, services and jobs by a system considered as a combination of technical information and human assets, in contrast to classical reliability which is generally limited to the analysis of technical resources and associated components and structures. The selection of papers for this volume illustrates the diversity of topics that need to be considered, from mathematical models and design methodologies through software engineering and data security issues to practical engineering problems in technical systems. In addition, this edition of the conference hosted the 7th CrISS DESSERT Workshop, which was devoted to the analysis and assurance of safety and cyber security in critical infrastructure and computer systems.

Silicon Photonics for High-Performance Computing and Beyond Mahdi Nikdast, Sudeep Pasricha, Gabriela Nicolescu, Ashkan Seyed, Di Liang, 2021-11-16 Silicon

photonics is beginning to play an important role in driving innovations in communication and computation for an increasing number of applications from health care and biomedical sensors to autonomous driving datacenter networking and security In recent years there has been a significant amount of effort in industry and academia to innovate design develop analyze optimize and fabricate systems employing silicon photonics shaping the future of not only Datacom and telecom technology but also high performance computing and emerging computing paradigms such as optical computing and artificial intelligence Different from existing books in this area Silicon Photonics for High Performance Computing and Beyond presents a comprehensive overview of the current state of the art technology and research achievements in applying silicon photonics for communication and computation It focuses on various design development and integration challenges reviews the latest advances spanning materials devices circuits systems and applications Technical topics discussed in the book include Requirements and the latest advances in high performance computing systems Device and system level challenges and latest improvements to deploy silicon photonics in computing systems Novel design solutions and design automation techniques for silicon photonic integrated circuits Novel materials devices and photonic integrated circuits on silicon Emerging computing technologies and applications based on silicon photonics Silicon Photonics for High Performance Computing and Beyond presents a compilation of 19 outstanding contributions from academic and industry pioneers in the field The selected contributions present insightful discussions and innovative approaches to understand current and future bottlenecks in high performance computing systems and traditional computing platforms and the promise of silicon photonics to address those challenges It is ideal for researchers and engineers working in the photonics electrical and computer engineering industries as well as academic researchers and graduate students M S and Ph D in computer science and engineering electronic and electrical engineering applied physics photonics and optics

Model-Based Design for Embedded Systems Gabriela Nicolescu, Pieter J. Mosterman, 2018-09-03 The demands of increasingly complex embedded systems and associated performance computations have resulted in the development of heterogeneous computing architectures that often integrate several types of processors analog and digital electronic components and mechanical and optical components all on a single chip As a result now the most prominent challenge for the design automation community is to efficiently plan for such heterogeneity and to fully exploit its capabilities A compilation of work from internationally renowned authors Model Based Design for Embedded Systems elaborates on related practices and addresses the main facets of heterogeneous model based design for embedded systems including the current state of the art important challenges and the latest trends Focusing on computational models as the core design artifact this book presents the cutting edge results that have helped establish model based design and continue to expand its parameters The book is organized into three sections Real Time and Performance Analysis in Heterogeneous Embedded Systems Design Tools and Methodology for Multiprocessor System on Chip and Design Tools and Methodology for Multidomain Embedded Systems The respective

contributors share their considerable expertise on the automation of design refinement and how to relate properties throughout this refinement while enabling analytic and synthetic qualities They focus on multi core methodological issues real time analysis and modeling and validation taking into account how optical electronic and mechanical components often interface Model based design is emerging as a solution to bridge the gap between the availability of computational capabilities and our inability to make full use of them yet This approach enables teams to start the design process using a high level model that is gradually refined through abstraction levels to ultimately yield a prototype When executed well model based design encourages enhanced performance and quicker time to market for a product Illustrating a broad and diverse spectrum of applications such as in the automotive aerospace health care consumer electronics this volume provides designers with practical readily adaptable modeling solutions for their own practice Embedded, Cyber-Physical, and IoT Systems Shuvra S. Bhattacharyya, Miodrag Potkonjak, Senem Velipasalar, 2019-06-29 This Festschrift is in honor of Marilyn Wolf on the occasion of her 60th birthday Prof Wolf is a renowned researcher and educator in Electrical and Computer Engineering who has made pioneering contributions in all of the major areas in Embedded Cyber Physical and Internet of Things IoT Systems This book provides a timely collection of contributions that cover important topics related to Smart Cameras Hardware Software Co Design and Multimedia applications Embedded systems are everywhere cyber physical systems enable monitoring and control of complex physical processes with computers and IoT technology is of increasing relevance in major application areas including factory automation and smart cities Smart cameras and multimedia technologies introduce novel opportunities and challenges in embedded cyber physical and IoT applications Advanced hardware software co design methodologies provide valuable concepts and tools for addressing these challenges The diverse topics of the chapters in this Festschrift help to reflect the great breadth and depth of Marilyn Wolf s contributions in research and education The chapters have been written by some of Marilyn s closest collaborators and colleagues

Integrated Interconnect Technologies for 3D Nanoelectronic Systems Muhannad S. Bakir, James D. Meindl, 2009 This cutting edge book on off chip technologies puts the hottest breakthroughs in high density compliant electrical interconnects nanophotonics and microfluidics at your fingertips integrating the full range of mathematics physics and technology issues together in a single comprehensive source You get full details on state of the art I O interconnects and packaging including mechanically compliant I O approaches fabrication and assembly followed by the latest advances and applications in power delivery design analysis and modeling The book explores interconnect structures materials and packages for achieving high bandwidth off chip electrical communication including optical interconnects and chip to chip signaling approaches and brings you up to speed on CMOS integrated optical devices 3D integration wafer stacking technology and through wafer interconnects *Optical Interconnects for Data Centers* Tolga Tekin, Nikos Pleros, Richard Pitwon, Andreas Hakansson, 2016-11-01 Current data centre networks based on electronic packet switches are experiencing

an exponential increase in network traffic due to developments such as cloud computing Optical interconnects have emerged as a promising alternative offering high throughput and reduced power consumption Optical Interconnects for Data Centers reviews key developments in the use of optical interconnects in data centres and the current state of the art in transforming this technology into a reality The book discusses developments in optical materials and components such as single and multi mode waveguides circuit boards and ways the technology can be deployed in data centres Optical Interconnects for Data Centers is a key reference text for electronics designers optical engineers communications engineers and R D managers working in the communications and electronics industries as well as postgraduate researchers Summarizes the state of the art in this emerging field Presents a comprehensive review of all the key aspects of deploying optical interconnects in data centers from materials and components to circuit boards and methods for integration Contains contributions that are drawn from leading international experts on the topic

Communication Architectures for Systems-on-Chip José L. Ayala, 2018-09-03 A presentation of state of the art approaches from an industrial applications perspective Communication Architectures for Systems on Chip shows professionals researchers and students how to attack the problem of data communication in the manufacture of SoC architectures With its lucid illustration of current trends and research improving the performance quality and reliability of transactions this is an essential reference for anyone dealing with communication mechanisms for embedded systems systems on chip and multiprocessor architectures or trying to overcome existing limitations Exploring architectures currently implemented in manufactured SoCs and those being proposed this book analyzes a wide range of applications including Well established communication buses Less common networks on chip Modern technologies that include the use of carbon nanotubes CNTs Optical links used to speed up data transfer and boost both security and quality of service QoS The book s contributors pay special attention to newer problems including how to protect transactions of critical on chip information personal data security keys etc from an external attack They examine mechanisms revise communication protocols involved and analyze overall impact on system performance

Low-Power CMOS Circuits Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design Low Power CMOS Circuits Technology Logic Design and CAD Tools addresses the design of low power circuitry in deep submicron technologies It provides a focused reference for specialists involved in designing low power circuitry from transistors to logic gates The book is organized into three broad sections for convenient access The first examines the history of low power electronics along with a look at emerging and possible future technologies It also considers other technologies such as nanotechnologies and optical chips that may be useful in designing integrated circuits The second part explains the techniques used to reduce power consumption at low levels These include clock gating leakage reduction interconnecting and communication on chips and adiabatic circuits The final section discusses various CAD tools

for designing low power circuits This section includes three chapters that demonstrate the tools and low power design issues at three major companies that produce logic synthesizers Providing detailed examinations contributed by leading experts Low Power CMOS Circuits Technology Logic Design and CAD Tools supplies authoritative information on how to design and model for high performance with low power consumption in modern integrated circuits It is a must read for anyone designing modern computers or embedded systems

Unveiling the Magic of Words: A Report on "**Integrated Optical Interconnect Architectures For Embedded Systems**"

In a global defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their capability to kindle emotions, provoke contemplation, and ignite transformative change is really awe-inspiring. Enter the realm of "**Integrated Optical Interconnect Architectures For Embedded Systems**," a mesmerizing literary masterpiece penned with a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve in to the book is central themes, examine its distinctive writing style, and assess its profound affect the souls of its readers.

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