

# Figure. 01

## Basic Logic Gates with Truth Table

### YES Gate



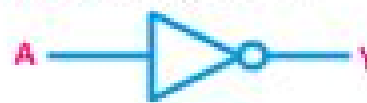
Boolean Expression:  $Y = A$

Truth Table:

| INPUT |  | OUTPUT (Y) |
|-------|--|------------|
| A     |  |            |
| 0     |  | 0          |
| 1     |  | 1          |

### NOT Gate

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$$Y = A'$$

| INPUT |  | OUTPUT (Y) |
|-------|--|------------|
| A     |  |            |
| 0     |  | 1          |
| 1     |  | 0          |

### AND Gate



$$Y = A \cdot B$$

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 0          |
| 0     | 1 | 0          |
| 1     | 0 | 0          |
| 1     | 1 | 1          |

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 1          |
| 0     | 1 | 1          |
| 1     | 0 | 1          |
| 1     | 1 | 0          |

### NAND Gate



$$Y = \overline{A \cdot B}$$

### OR Gate



$$Y = A + B$$

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 0          |
| 0     | 1 | 1          |
| 1     | 0 | 1          |
| 1     | 1 | 1          |

### NOR Gate



$$Y = \overline{A + B}$$

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 1          |
| 0     | 1 | 0          |
| 1     | 0 | 0          |
| 1     | 1 | 0          |

### XOR Gate



$$Y = A \oplus B$$

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 0          |
| 0     | 1 | 1          |
| 1     | 0 | 1          |
| 1     | 1 | 0          |

| INPUT |   | OUTPUT (Y) |
|-------|---|------------|
| A     | B |            |
| 0     | 0 | 1          |
| 0     | 1 | 0          |
| 1     | 0 | 0          |
| 1     | 1 | 1          |

### XNOR Gate



$$Y = \overline{A \oplus B}$$

# Logic Design Study Guide

**CO Houle**



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