

blue: signals to outside world

red: control signals latched by datain => CPU_S2
or
control signals driven by CPU_S1 for opcode
latching

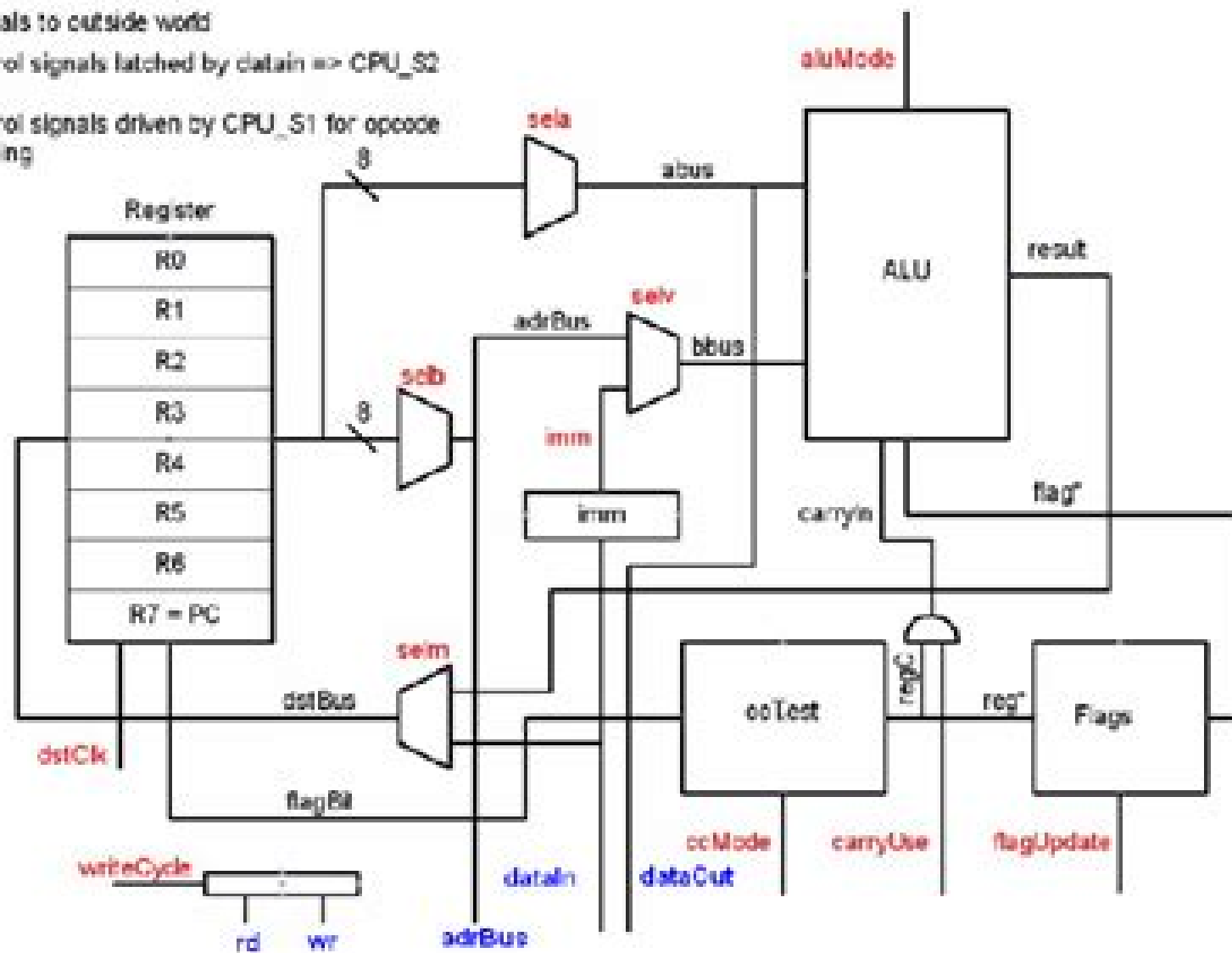


Figure 3. Data path for 64-bit RISC Processor [5]

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