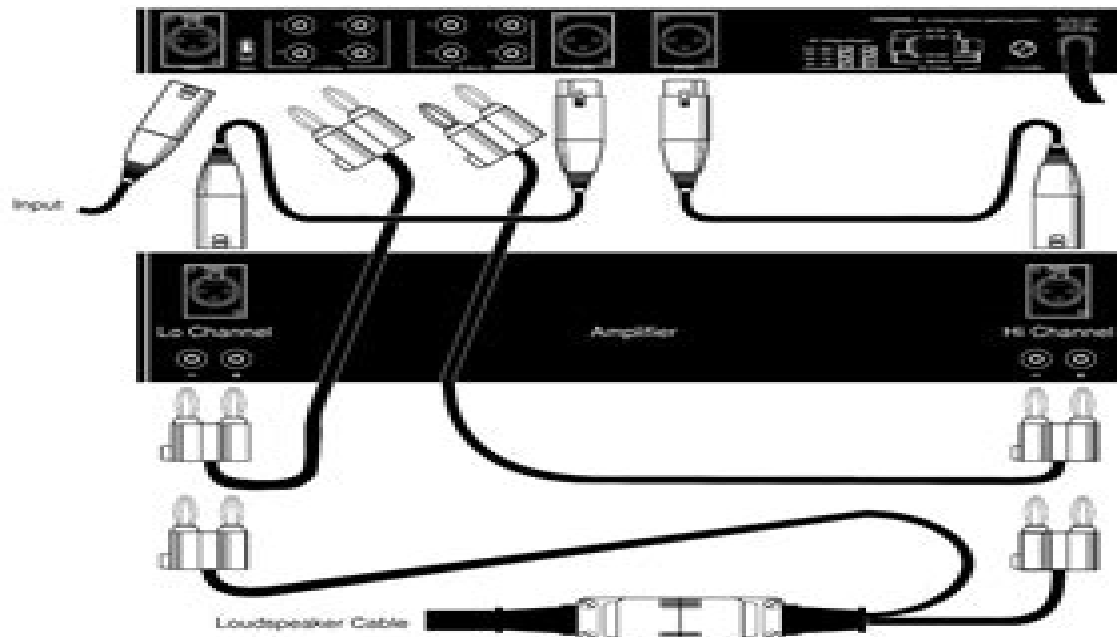


The Meyer Sound S-1 is a single-channel active signal processor designed for use with the USM-1 Loudspeaker. It occupies a single 1 1/2-inch rack space.

The functions of the S-1 are:

- Active crossover for bi-amplification
- SpeakerSense™ driver protection with MultiSense™ function
- True excursion limiting to protect the USM-1 drivers
- Loudspeaker frequency and phase response alignment



Connections

The S-1 operates at line level and is intended to be the final component in the signal chain before the power amplifier. Connections between the S-1 and the power amplifier should be made according to the diagram above.

1. **Signal inputs** to the S-1 may be either balanced or unbalanced. For best signal-to-noise ratio, use balanced connections operating at +4 dBu nominal. The S-1 will accept peak input levels up to +26 dBu balanced, or +20 dBu unbalanced.

Note: The S-1 utilizes Meyer Sound's exclusive ISO™ input. Pins 1, 2 and 3 are transformer-isolated, and the connector shell is connected to earth ground. The Input GND/ Lift switch controls the connection between pin 1 and circuit common, which is tied to AC/chassis ground through a 500 ohm resistance. If hum problems occur, this switch may be used to control ground loops in the system.

2. **SpeakerSense™** connections are made from the output of the power amplifier back to the S-1 Sense inputs. The Hi output of the power amplifier must be connected to the Hi Sense input, and the Lo output of the power amplifier to the Lo Sense input.

The Sense connection **must** be made in order for the driver protection circuitry to operate properly.

Note: The S-1 Sense inputs incorporate Meyer Sound's exclusive MultiSense™ function, which allows use of multiple power amplifiers driven in parallel from a single S-1 and having different voltage gains and/or power ratings. The S-1 accommodates up to two power amplifiers, and provides a separate Sense input for each. The MultiSense circuit automatically tracks the power amplifier with the greatest output swing to control the system protection circuitry. These inputs are polarity-sensitive; be certain that they are connected with correct polarity, as indicated on the S-1 rear panel.

3. **Signal outputs** from the S-1 are active balanced at +4 dBu nominal operating level, with pin 1 tied to earth/chassis ground through a 500 ohm resistance. The maximum output level is +26 dBu balanced (+20 dBu unbalanced).

4. Connections between the power amplifier output and the USM-1 should be made in accordance with the **USM-1 Operating Instructions**. These connections **must** be verified for correct polarity and correct channel assignment (Hi to Hi, Lo to Lo).

Meyer Electronics Solution Manual

Thomas Henry O'Dell



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Books and Pamphlets, Including Serials and Contributions to Periodicals Library of Congress. Copyright Office, 1968 Electromigration in ULSI Interconnections Cher Ming Tan, 2010 Electromigration in ULSI Interconnections provides a comprehensive description of the electro migration in integrated circuits It is intended for both beginner and advanced readers on electro migration in ULSI interconnections It begins with the basic knowledge required for a detailed study on electro migration and examines the various interconnected systems and their evolution employed in integrated circuit technology The subsequent chapters provide a detailed description of the physics of electro migration in both Al and Cu based Interconnections in the form of theoretical experimental and numerical modeling studies The differences in the electro migration of Al and Cu based interconnections and the corresponding underlying physical mechanisms for these differences are explained The test structures testing methodology failure analysis methodology and statistical analysis of the test data for the experimental studies on electro migration are presented in a concise and rigorous manner Methods of numerical modeling for the interconnect electro migration and their applications to the understanding of electro migration physics are described in detail with the aspects of material properties interconnection design and interconnect process parameters on the electro migration performances of interconnects in ULSI further elaborated upon Finally the extension of the studies to narrow interconnections is introduced and future challenges on the study of electro migration are outlined and discussed *Catalog of Copyright Entries. Third Series* Library of Congress. Copyright Office, 1968 *Device Electronics for Integrated Circuits* Richard S. Muller, Theodore I. Kamins, 1977 Focusing specifically on silicon devices the Third Edition of *Device Electronics for Integrated Circuits* takes students in integrated circuits courses from fundamental physics to detailed device operation Because the book focuses primarily on silicon devices each topic can include more depth and extensive worked examples and practice problems ensure that students understand the details **The Publishers' Trade List Annual** , 1987 **Catalog of Copyright Entries. Third Series** Library of Congress. Copyright Office, 1966

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