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EE – 605

(Following Paper ID and Roll No. to be filled in your Answer Book)

**PAPER ID : 3044**

Roll No. 

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**B. Tech.**

**(SEM. VI) EXAMINATION. 2006-07**

**MICROPROCESSOR & ITS APPLICATION**

*Time : 3 Hours]*

*[Total Marks : 100*

- Note :**
- (1) *Attempt all questions.*
  - (2) *Make and state appropriate assumptions where necessary.*

**1** Answer any **four** parts : **5×4=20**

- (a) What is a microprocessor? When it is said that a certain microprocessor is a 32 bit microprocessor, what does it exactly mean? Using this indicator, classify the common Intel microprocessors from earliest history. How many pins does 8086 have? Give the number only.
- (b) What are the commonly used alpha-numeric codes in a computer? Why are such codes needed? Compare the codes you have listed.
- (c) How many segment registers are provided in 8086? Are so many required? Justify.
- (d) Do you agree with the following statement ? “8086 may have 64K segments. Each segment may be upto 64 kByte. Thus physical memory address space goes upto 64K x 64k Byte = 4 G Byte.” Explain your answer.

# Microprocessor And Its Applications Question Paper

**Mr. Rohit Manglik**



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**Microprocessors and Their Applications** Jacques Tiberghien,Gunnar Carlstedt,Johan Lewi,1979 Languages and language processors Display systems Performance improvements by microcod Panel on development systems Microprocessor utilisation in data base systems Tools for microprogram development Operating systems Data logging The use of microprocessors in communications Special purpose processors *Proceedings of Transpac '84* ,1985 *Algorithm Theory - SWAT 2004* Torben Hagerup,Jyrki Katajainen,2004-06-08 This volume contains the papers presented at SWAT 2004 the 9th Scandi vian Workshop on Algorithm Theory which was held on July 8 10 2004 at the Louisiana Museum of Modern Art in Humleb k on the resund coast north of Copenhagen The SWAT workshop in reality a full edged conference has been held

biennially since 1988 and rotates among the five Nordic countries Denmark Finland Iceland Norway and Sweden The previous meetings took place in Halmstad 1988 Bergen 1990 Helsinki 1992 Aarhus 1994 Reykjavik 1996 Stockholm 1998 Bergen 2000 and Turku 2002 SWAT alternates with the Workshop on Algorithms and Data Structures WADS held in odd-numbered years The call for papers invited contributions on all aspects of algorithm theory A total of 121 submissions was received an overall SWAT High These underwent thorough reviewing and the program committee met in Copenhagen on March 20-21 2004 and selected 40 papers for presentation at the conference The program committee was impressed with the quality of the submissions and given the constraints imposed by the choice of conference venue and duration had to make some tough decisions The scientific program was enriched by invited presentations by Gerth Stølting Brodal University of Aarhus and Charles E. Leiserson Massachusetts Institute of Technology Two satellite events were held immediately before SWAT 2004 the Workshop on On-Line Algorithms OLA 2004 organized by members of the Department of Mathematics and Computer Science at the University of Southern Denmark and the Summer School on Experimental Algorithmics organized by the Performance Engineering Laboratory in the Department of Computing at the University of Copenhagen More information about SWAT 2004 and its satellite events is available at the conference web site <http://swat.diku.dk>

**Computerworld**, 1974-09-04 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld's award-winning Web site Computerworld.com twice monthly publication focused conference series and custom research form the hub of the world's largest global IT media network

Annual Review in Automatic Programming M. I. Halpern, 2014-05-18 Annual Review in Automatic Programming Volume 9 deals with automatic programming with emphasis on the programming language ALGOL 68 The book demonstrates the progression in the formal definition of programming languages from ALGOL 60 through to the revised version of ALGOL 68 Other topics range from real-time operating systems and process control languages to data flow analysis microprocessors design automation real-time system specifications and Fortran real-time programming After an introduction to the formal definition of ALGOL 68 this volume turns to an operating system which provides for a flexible interface to support a wide spectrum of real-time language facilities The chapters that follow focus on data flow through the elements of a real-time application the possibilities as well as the problems of future microprocessor applications in real-time systems and the design goals and main principles of a monitor called SIMON A language based upon an automaton model is also described with particular reference to synchronizations between actions and events In addition the book presents a pilot study of the possibility to develop an integrated interactive system for computer-aided design of control computer systems and considers the industrial real-time BASIC designed for process control A chapter discussing the unique distributed DDC system developed by Tokyo Gas Company and Hitachi for city gas production plants concludes the book This book will be of use to students and professionals interested in programming languages

**Performance-oriented Application Development for Distributed Architectures** M. Gerndt, 2002

Annotation This publication is devoted to programming models languages and tools for performance oriented program development in commercial and scientific environments The included papers have been written based on presentations given at the workshop PADDA 2001 The goal of the workshop was to identify common interests and techniques for performance oriented program development in commercial and scientific environments Distributed architectures currently dominate the field of highly parallel computing Distributed architectures based on Internet and mobile computing technologies are important target architectures in the domain of commercial computing too The papers in this publication come from the two areas scientific computing and commercial computing

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