

Practical 1

Date: 3rd Jan.

Aim: To Study about Microwind tool and λ (Lambda) Rules for Layout Generation.

Objective:

1. To be familiar with tool.
2. To learn about λ (Lambda) Rules for 90 nm Technology.

Microwind Getting Started:

The present experiment is a guide to using the « **Microwind** » educational software on a PC computer.

The MICROWIND program allows the student to design and simulate an integrated circuit. The package itself contains a library of common logic and analog ICs to view and simulate. MICROWIND includes all the commands for a mask editor as well as new original tools never gathered before in a single module. You can gain access to *Circuit Simulation* by pressing one single key. The electric extraction of your circuit is automatically performed and the analog simulator produces voltage and current curves immediately.

A specific command displays the characteristics of pMOS and nMOS, where the size of the device and the process parameters can be very easily changed. Altering the MOS model parameters and, then, seeing the effects on the V_d and I_d curves constitutes a good interactive tutorial on devices.

The *Process Simulator* shows the layout in a vertical perspective, as when fabrication has been completed. This feature is a significant aid to supplement the descriptions of fabrication found in most textbooks.

The *Logic Cell Compiler* is a particularly sophisticated tool enabling the automatic design of a CMOS circuit corresponding to your logic description in VERILOG. The DSCH software, which is a user-friendly schematic editor and a logic simulator presented in a companion manual, is used to generate this Verilog description. The cell is created in compliance with the environment, design rules and fabrication specifications.

A set of CMOS processes ranging from 1.2 μ m down to state-of-the-art 0.25 μ m are proposed.

To use the MICROWIND program use the following procedure:

- ❶ Go to the directory in which the software has been copied

Microwind Lab Manual

Clemens Wendtner



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Table of Contents Microwind Lab Manual

1. Understanding the eBook Microwind Lab Manual
 - The Rise of Digital Reading Microwind Lab Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Microwind Lab Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microwind Lab Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microwind Lab Manual
 - Personalized Recommendations
 - Microwind Lab Manual User Reviews and Ratings
 - Microwind Lab Manual and Bestseller Lists
5. Accessing Microwind Lab Manual Free and Paid eBooks

- Microwind Lab Manual Public Domain eBooks
- Microwind Lab Manual eBook Subscription Services
- Microwind Lab Manual Budget-Friendly Options
- 6. Navigating Microwind Lab Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Microwind Lab Manual Compatibility with Devices
 - Microwind Lab Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microwind Lab Manual
 - Highlighting and Note-Taking Microwind Lab Manual
 - Interactive Elements Microwind Lab Manual
- 8. Staying Engaged with Microwind Lab Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microwind Lab Manual
- 9. Balancing eBooks and Physical Books Microwind Lab Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microwind Lab Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Microwind Lab Manual
 - Setting Reading Goals Microwind Lab Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Microwind Lab Manual
 - Fact-Checking eBook Content of Microwind Lab Manual
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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