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Question Paper Code : X10405

**B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2020 AND
APRIL/MAY 2021**

Sixth/Seventh Semester

Electronics and Instrumentation Engineering

EE 8691 – EMBEDDED SYSTEMS

**(Common to Electrical and Electronics Engineering)
(Regulations 2017)**

Time : Three Hours

Maximum : 100 Marks

Answer ALL questions

PART – A

(10×2=20 Marks)

1. Define different categories and requirements of embedded systems.
2. Name some of the hardware parts of embedded systems.
3. Differentiate synchronous communication and Iso-synchronous communication.
4. Draw the data frame format of CAN.
5. Classify the difference between state machine model and sequential model.
6. Draw the classic embedded product development life cycle model.
7. Distinguish non preemptive and preemptive scheduling in RTOS.
8. Justify the advantages of mail boxes in RTOS.
9. Mention the importance of input interface and output interface in Washing machine.
10. State the role of interfacing I/O circuit in smart card.

PART – B

(5×13=65 Marks)

11. a) With neat diagram, explain the working of Direct Memory Access (DMA) and mention the memory management methods.

(OR)

- b) i) Explain how suitable processor and memory devices are selected for an embedded system design. **(6)**
- ii) Explain the function of timing and counting devices in embedded systems. **(7)**

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Judd E. Hollander



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Table of Contents Embedded Systems Question Paper For Eee

1. Understanding the eBook Embedded Systems Question Paper For Eee
 - The Rise of Digital Reading Embedded Systems Question Paper For Eee
 - Advantages of eBooks Over Traditional Books
2. Identifying Embedded Systems Question Paper For Eee
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Embedded Systems Question Paper For Eee
 - User-Friendly Interface
4. Exploring eBook Recommendations from Embedded Systems Question Paper For Eee
 - Personalized Recommendations
 - Embedded Systems Question Paper For Eee User Reviews and Ratings
 - Embedded Systems Question Paper For Eee and Bestseller Lists

5. Accessing Embedded Systems Question Paper For Eee Free and Paid eBooks
 - Embedded Systems Question Paper For Eee Public Domain eBooks
 - Embedded Systems Question Paper For Eee eBook Subscription Services
 - Embedded Systems Question Paper For Eee Budget-Friendly Options
6. Navigating Embedded Systems Question Paper For Eee eBook Formats
 - ePub, PDF, MOBI, and More
 - Embedded Systems Question Paper For Eee Compatibility with Devices
 - Embedded Systems Question Paper For Eee Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Embedded Systems Question Paper For Eee
 - Highlighting and Note-Taking Embedded Systems Question Paper For Eee
 - Interactive Elements Embedded Systems Question Paper For Eee
8. Staying Engaged with Embedded Systems Question Paper For Eee
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Embedded Systems Question Paper For Eee
9. Balancing eBooks and Physical Books Embedded Systems Question Paper For Eee
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Embedded Systems Question Paper For Eee
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Embedded Systems Question Paper For Eee
 - Setting Reading Goals Embedded Systems Question Paper For Eee
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Embedded Systems Question Paper For Eee
 - Fact-Checking eBook Content of Embedded Systems Question Paper For Eee
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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