

An Introduction to Parallel Programming Solutions, Chapter 1

Jinyoung Choi and Peter Pacheco

February 1, 2011

```
1.    quotient = n / p;
      remainder = n % p;
      if (my_rank < remainder) {
          my_n_count = quotient + 1;
          my_first_i = my_rank * my_n_count;
      } else {
          my_n_count = quotient;
          my_first_i = my_rank * my_n_count + remainder;
      }
      my_last_i = my_first_i + my_n_count;
```

2. We are assigning blocks of elements to cores in order (the first n/p elements to core 0, the next n/p elements to core 1, so on). So, for example, if $n = 12$ and $p = 4$, core 0 spends 12 milliseconds in the call to `Compute_next_value` ($i = 0, 1, 2$), core 1 spends 30 milliseconds ($i = 3, 4, 5$), core 2 spends 48 milliseconds ($i = 6, 7, 8$), and core 3 spends 66 milliseconds ($i = 9, 10, 11$). So clearly this assignment will do a very poor job of load balancing.

A better approach uses a *cyclic* assignment of the work to the cores:

```
/* assign[c][j] is the jth value of i assigned to core c */
/* work[c] is the total amount of work assigned to core c */
c = j = 0;
for (i = 0; i < n; i++) {
    work[c] += 2*(i+1);
    assign[c][j] = i;
    c = (c + 1) % p;
    if (c == 0) j++;
}
```

Introduction To Parallel Processing Solution Manual

Vipin Kumar



Introduction To Parallel Processing Solution Manual:

Introduction to Parallel Computing Vipin Kumar, 2001-07-01 **Euro-Par 2002. Parallel Processing** Burkhard Monien, 2002-08-21 This book constitutes the refereed proceedings of the 8th European Conference on Parallel Computing Euro Par 2002 held in Paderborn Germany in August 2002 The 67 revised full papers and 55 research note papers presented together with 6 invited papers were carefully reviewed and selected from 265 submissions The papers presented give a unique survey of the state of the art in parallel computing research ranging from algorithms software hardware and application in various fields Digital Design from Zero to One Jerry D. Daniels, 1996-04-19 Takes a fresh look at basic digital design From definition to example to graphic illustration to simulation result the book progresses through the main themes of digital design Technically up to date this book covers all the latest topics Field programmable gate arrays PALs and ROMs The latest memory chips for SRAM and DRAM are shown Software for creating the excitation equations of FSM are covered as well as LogicWorks and Beige Bag PC and more Euro-Par 2004 Parallel Processing Marco Danelutto, Marco Vanneschi, Domenico Laforenza, 2004-12-27 Euro Par Conference Series Euro Par is an annual series of international conferences dedicated to the promotion and advancement of all aspects of parallel computing The major themes can be divided into the broad categories of hardware software algorithms and applications for parallel computing The objective of Euro Par is to provide a forum within which to promote the development of parallel computing both as an industrial technique and an academic discipline extending the frontier of both the state of the art and the state of the practice This is particularly important at a time when parallel computing is undergoing strong and sustained development and experiencing real industrial take up The main audience for and participants at Euro Par are seen as researchers in academic departments government laboratories and industrial organizations Euro Par's objective is to be the primary choice of such professionals for the presentation of new results in their specific areas Euro Par also targets applications demonstrating the effectiveness of parallelism This year's Euro Par conference was the tenth in the conference series The previous Euro Par conferences took place in Stockholm Lyon Passau Southampton Toulouse Munich Manchester Paderborn and Klagenfurt Next year the conference will take place in Lisbon Euro Par has a permanent Web site hosting the aims the organization structure details as well as all the conference history <http://www.europar.org> *Parallel Processing and Applied Mathematics* Roman Wyrzykowski, Jack Dongarra, Konrad Karczewski, Jerzy Wasniewski, 2012-07-03 This two volume set LNCS 7203 and 7204 constitutes the refereed proceedings of the 9th International Conference on Parallel Processing and Applied Mathematics PPAM 2011 held in Torun Poland in September 2011 The 130 revised full papers presented in both volumes were carefully reviewed and selected from numerous submissions The papers address issues such as parallel distributed architectures and mobile computing numerical algorithms and parallel numerics parallel non numerical algorithms tools and environments for parallel distributed grid computing applications of parallel distributed computing applied mathematics neural networks and

evolutionary computing history of computing *Analysis and Design of Scalable Parallel Algorithms for Scientific Computing* Anshul Gupta,1995 **Proceedings of the Seventh SIAM Conference on Parallel Processing for Scientific Computing** David H. Bailey,1995-01-01 Proceedings Parallel Computing **Concise Encyclopedia of Software Engineering** Derrick Morris,2013-10-22 This Concise Encyclopedia of Software Engineering is intended to provide compact coverage of the knowledge relevant to the practicing software engineer The content has been chosen to provide an introduction to the theory and techniques relevant to the software of a broad class of computer applications It is supported by examples of particular applications and their enabling technologies This Encyclopedia will be of value to new practitioners who need a concise overview and established practitioners who need to read about the penumbra surrounding their own specialities It will also be useful to professionals from other disciplines who need to gain some understanding of the various aspects of software engineering which underpin complex information and control systems and the thinking behind them

Optimization, Parallel Processing and Applications Alexander Kurzhanski,Klaus Neumann,Diethard Pallaschke,2012-12-06 This volume contains selected papers presented either at the Oberwolfach Conference on Operations Research February 1987 or at the three day workshop on Advanced Computation Techniques Parallel Processing and Optimization organized by IIASA and the University of Karlsruhe which immediately followed The aim of the conferences was to discuss recently developed methods in optimization theory and their practical implementation using advanced computation techniques especially in parallel processing The volume is divided into five sections I Algorithms and Optimization Methods II Optimization and Parallel Processing III Graph Theory and Scheduling IV Differential Equations and Operator Theory V Applications **Transputer Applications and Systems '93** Reinhard Grebe,1993 Proceedings Parallel Computing

Systems of Insight Overview Hector H. Diaz Lopez,IBM Redbooks,2015-11-17 Decision making is a critical function in any enterprise The decision making process that is enhanced by analytics can be described as consuming and collecting data detecting relationships and patterns applying sophisticated analysis techniques reporting and automation of the follow on action The IT system that supports decision making is composed of the traditional systems of record systems of engagement and the systems of insight This IBM Redbooks Solution Guide introduces the concept of systems of insight based on what is detailed in the IBM Redbooks publication Systems of Insight for Digital Transformation SG24 8293 found at <http://www.redbooks.ibm.com/redpieces/abstracts/sg248293.html> Open *Catalog of Copyright Entries. Third Series* Library of Congress. Copyright Office,1976 **Computer Organization, Design, and Architecture** Sajjan G. Shiva,2025-05-30 This unique and classroom proven text provides a hands on introduction to the design of computer systems It depicts step by step the design and programming of a simple but complete hypothetical computer followed by detailed architectural features of existing computer systems as enhancements to the structure of the simple computer This treatment integrates the four categories of digital systems architecture logic design computer organization computer hardware and computer system

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