

[4317]-22

F.Y. B.Sc.

ELECTRONIC SCIENCE

EL1 - T2 : Principles of Digital Electronics
(2008 Pattern) (Paper - II)

Time : 3 Hours]

[Max. Marks : 80

Instructions to the candidates:

- 1) All questions are compulsory.
- 2) Neat labelled diagrams must be drawn wherever necessary.
- 3) Use calculator and log table is allowed.
- 4) Figures to the right indicate full marks.

Q1) Answer the following questions in brief :

[16]

- a) What is the radix used in the case of binary and hexadecimal?
- b) Why NAND and NOR gates are called as universal gate?
- c) Draw logic diagram for logic equation $Y = (A + B).C$
- d) Subtract (1001) from (1100) using the 1's complement method.
- e) Define :
 - i) Combinational logic circuit.
 - ii) Sequential logic circuit.
- f) Draw logic diagram and write truth table for T - flip - flop.
- g) Define the terms noise immunity and noise margin.
- h) State the basic principle of operation of static RAM cell and Dynamic RAM cell.

Q2) Answer any four of the following :

[16]

- a) Convert the following.
 - i) $(101110)_2 = (\dots\dots\dots)_{Gray}$
 - ii) $(0110)_{Gray} = (\dots\dots\dots)_2$
- b) Draw logic diagram of two input OR gate using diode. Explain its action.
- c) Simplify following expression using K - Map.
$$Y = \overline{A}BCD + A\overline{B}CD + ABC\overline{D} + ABCD$$
- d) With the neat diagram explain working of parallel 4 - bit binary subtractor.
- e) Draw neat diagram of key - board encoder. Explain its operation with truth table.
- f) Explain working of clocked R - S flip - flop with logic diagram and truth table.

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Table of Contents Iti Basic Electronics Objective Type Questions And Answers

1. Understanding the eBook Iti Basic Electronics Objective Type Questions And Answers
 - The Rise of Digital Reading Iti Basic Electronics Objective Type Questions And Answers
 - Advantages of eBooks Over Traditional Books
2. Identifying Iti Basic Electronics Objective Type Questions And Answers
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Iti Basic Electronics Objective Type Questions And Answers
 - User-Friendly Interface
4. Exploring eBook Recommendations from Iti Basic Electronics Objective Type Questions And Answers
 - Personalized Recommendations

- Iti Basic Electronics Objective Type Questions And Answers User Reviews and Ratings
- Iti Basic Electronics Objective Type Questions And Answers and Bestseller Lists
- 5. Accessing Iti Basic Electronics Objective Type Questions And Answers Free and Paid eBooks
 - Iti Basic Electronics Objective Type Questions And Answers Public Domain eBooks
 - Iti Basic Electronics Objective Type Questions And Answers eBook Subscription Services
 - Iti Basic Electronics Objective Type Questions And Answers Budget-Friendly Options
- 6. Navigating Iti Basic Electronics Objective Type Questions And Answers eBook Formats
 - ePub, PDF, MOBI, and More
 - Iti Basic Electronics Objective Type Questions And Answers Compatibility with Devices
 - Iti Basic Electronics Objective Type Questions And Answers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Iti Basic Electronics Objective Type Questions And Answers
 - Highlighting and Note-Taking Iti Basic Electronics Objective Type Questions And Answers
 - Interactive Elements Iti Basic Electronics Objective Type Questions And Answers
- 8. Staying Engaged with Iti Basic Electronics Objective Type Questions And Answers
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Iti Basic Electronics Objective Type Questions And Answers
- 9. Balancing eBooks and Physical Books Iti Basic Electronics Objective Type Questions And Answers
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Iti Basic Electronics Objective Type Questions And Answers
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Iti Basic Electronics Objective Type Questions And Answers
 - Setting Reading Goals Iti Basic Electronics Objective Type Questions And Answers
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Iti Basic Electronics Objective Type Questions And Answers
 - Fact-Checking eBook Content of Iti Basic Electronics Objective Type Questions And Answers

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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