ACALL addr11	DIV AB	LJMP addr16	RETI
ADD A, <src< td=""><td>DJNZ <byte, <re18<="" td=""><td>MOV <dest,<src< td=""><td>RL A</td></dest,<src<></td></byte,></td></src<>	DJNZ <byte, <re18<="" td=""><td>MOV <dest,<src< td=""><td>RL A</td></dest,<src<></td></byte,>	MOV <dest,<src< td=""><td>RL A</td></dest,<src<>	RL A
ADDC A, <src< td=""><td>INC <byte< td=""><td>MOV DPTR,#data16</td><td>RLC A</td></byte<></td></src<>	INC <byte< td=""><td>MOV DPTR,#data16</td><td>RLC A</td></byte<>	MOV DPTR,#data16	RLC A
AJMP addr11	INC DPTR	MOV bit,bit	RR A
ANL <dest,<src< td=""><td>JB bit, rel8</td><td>MOVC A, @A+<base< td=""><td>RRC A</td></base<></td></dest,<src<>	JB bit, rel8	MOVC A, @A+ <base< td=""><td>RRC A</td></base<>	RRC A
ANL <bit< td=""><td>JBC bit, rel8</td><td>MOVX <dest,<src< td=""><td>SETB bit</td></dest,<src<></td></bit<>	JBC bit, rel8	MOVX <dest,<src< td=""><td>SETB bit</td></dest,<src<>	SETB bit
CJNE <dest,<src,rel8< td=""><td>JC rel8</td><td>MUL AB</td><td>SJMP rel8</td></dest,<src,rel8<>	JC rel8	MUL AB	SJMP rel8
CLR A	JMP @A+DPTR	NOP	SUBB A, <src< td=""></src<>
CLR bit	JNB bit, rel8	ORL <dest,<src< td=""><td>SWAP A</td></dest,<src<>	SWAP A
CPL A	JNC rel8	ORL C, bit	XCH A, <byte< td=""></byte<>
CPL bit	JNZ rel8	POP direct	XCHD A, @Ri
DA A	JZ rel8	PUSH direct	XRL <dest,<src< td=""></dest,<src<>
DEC <byte< td=""><td>LCALL addr16</td><td>RET</td><td></td></byte<>	LCALL addr16	RET	

# **Move Instruction In 8051**

**Ghoshal Subrata** 

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