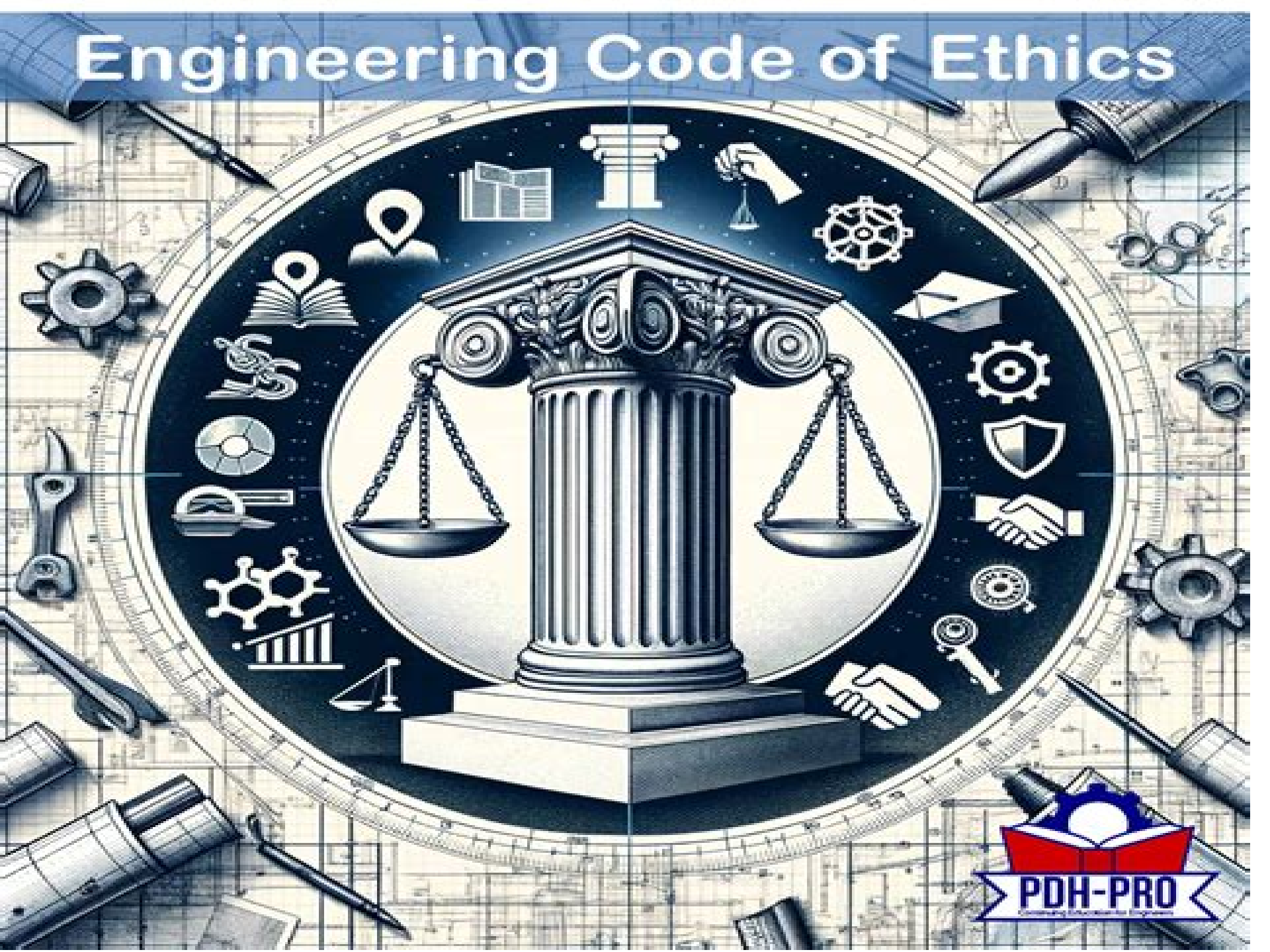


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# Ethics In Engineering

**SJ Ball**



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software selectable high and low voltage reference input to some combination of vdd vss ra2 and ra3 the analog input to pic is limited to vss and vdd voltages 0 5v of pic this circuit is designed to measure 0 to 30v

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