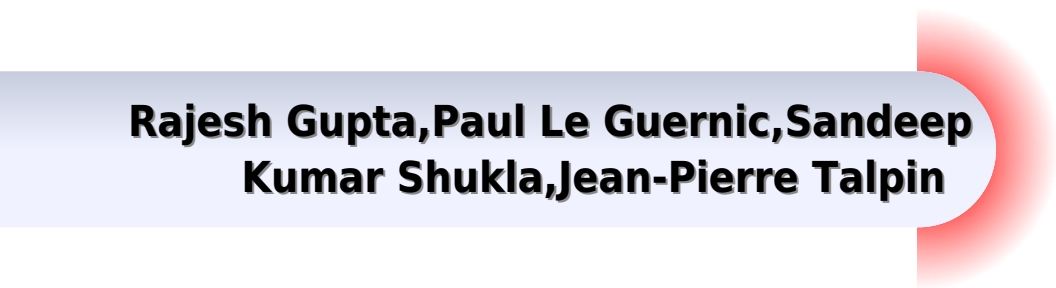


Ivan Radojevic · Zoran Salcic

Embedded Systems Design Based on Formal Models of Computation

Embedded Systems Design Based On Formal Models Of Computation

**Rajesh Gupta, Paul Le Guernic, Sandeep
Kumar Shukla, Jean-Pierre Talpin**



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Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15
Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts. It targets heterogeneous systems by combining finite state machines (FSM) with synchronous dataflow graphs (SDFG). FSMs are connected in the same way as in Argos, a Statecharts variant with purely synchronous semantics using three operators: synchronous parallel, refinement, and hiding. The fourth operator called asynchronous parallel is introduced in DFCharts to connect FSMs with SDFGs. In the formal semantics of DFCharts, the operation of an SDFG is represented as an FSM. Using this representation, SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM. This allows system properties to be verified globally. The practical application of DFCharts has been demonstrated by linking it to widely used system level languages: Java Esterel and SystemC.

Model-Based Design for Embedded Systems Gabriela Nicolescu, Pieter J. Mosterman, 2018-09-03
The demands of increasingly complex embedded systems and associated performance computations have resulted in the development of heterogeneous computing architectures that often integrate several types of processors: analog and digital, electronic components and mechanical and optical components, all on a single chip. As a result, now the most prominent challenge for the design automation community is to efficiently plan for such heterogeneity and to fully exploit its capabilities. A compilation of work from internationally renowned authors, *Model Based Design for Embedded Systems* elaborates on related practices and addresses the main facets of heterogeneous model based design for embedded systems, including the current state of the art, important challenges, and the latest trends. Focusing on computational models as the core design artifact, this book presents the cutting edge results that have helped establish model based design and continue to expand its parameters. The book is organized into three sections: Real Time and Performance Analysis in Heterogeneous Embedded Systems, Design Tools and Methodology for Multiprocessor System on Chip, and Design Tools and Methodology for Multidomain Embedded Systems. The respective contributors share their considerable expertise on the automation of design refinement and how to relate properties throughout this refinement while enabling analytic and synthetic qualities. They focus on multi-core methodological issues: real time analysis and modeling and validation, taking into account how optical, electronic, and mechanical components often interface. Model based design is emerging as a solution to bridge the gap between the availability of computational capabilities and our inability to make full use of them yet. This approach enables teams to start the design process using a high level model that is gradually refined through abstraction levels to ultimately yield a prototype. When executed well, model based design encourages enhanced performance and quicker time to market for a product. Illustrating a broad and diverse spectrum of applications such as in the automotive, aerospace, health care, consumer electronics, this volume provides designers with practical, readily adaptable modeling solutions for their

own practice **Design Principles for Embedded Systems** KCS Murti, 2021-09-20 The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science This book attempts to bridge the gap between electronics and computer science students providing complementary knowledge that is essential for designing an embedded system The book covers key concepts tailored for embedded system design in one place The topics covered in this book are models and architectures Executable Specific Languages SystemC Unified Modeling Language real time systems real time operating systems networked embedded systems Embedded Processor architectures and platforms that are secured and energy efficient A major segment of embedded systems needs hard real time requirements This textbook includes real time concepts including algorithms and real time operating system standards like POSIX threads Embedded systems are mostly distributed and networked for deterministic responses The book covers how to design networked embedded systems with appropriate protocols for real time requirements Each chapter contains 2-3 solved case studies and 10 real world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs **Software Engineering and Formal Methods** Alessandro Cimatti, Marjan Sirjani, 2017-08-24 This book constitutes the refereed proceedings of the 15th International Conference on Software Engineering and Formal Methods SEFM 2017 held in Trento Italy in September 2017 The 17 full papers and 6 short papers presented were carefully reviewed and selected from 102 submissions The papers deal with a large range of topics in the following research areas new frontiers in software architecture software verification and testing software development methods application and technology transfer security and safety and design principles **Formal Methods and Models for System Design** Rajesh Gupta, Paul Le Guernic, Sandeep Kumar Shukla, Jean-Pierre Talpin, 2004-06-30 Perhaps nothing characterizes the inherent heterogeneity in embedded systems than the ability to choose between hardware and software implementations of a given system function Indeed most embedded systems at their core represent a careful division and design of hardware and software parts of the system To do this task effectively models and methods are necessary functionality to capture application behavior needs and system implementation constraints Formal modeling can be valuable in addressing these tasks As with most engineering domains co design practice defines the state of the it seeks to add new capabilities in system conceptualization model transformation optimization and implementation These advances particularly those related to synthesis and verification tasks directly depend upon formal understanding of system behavior and performance measures Current practice in system modeling relies upon exploiting high level programming frameworks such as SystemC Esterel to capture design at increasingly higher levels of abstraction and attempts to reduce the system implementation task While raising the abstraction levels for design and verification tasks to be really useful these approaches must also provide for reuse/adaptation of the existing intellectual property IP blocks **Integrated Formal Methods** Jim

Davies,Jeremy Gibbons,2007-09-14 This book constitutes the refereed proceedings of the 6th International Conference on Integrated Formal Methods IFM 2007 held in Oxford UK It addresses all aspects of formal methods integration including of a process of analysis or design application of formal methods to analysis or design extension of one method based upon the inclusion of ideas or concepts from others and semantic integration or practical application *Low-Power NoC for High-Performance SoC Design* Hoi-Jun Yoo,Kangmin Lee,Jun Kyong Kim,2018-10-08 Chip Design and Implementation from a Practical Viewpoint Focusing on chip implementation Low Power NoC for High Performance SoC Design provides practical knowledge and real examples of how to use network on chip NoC in the design of system on chip SoC It discusses many architectural and theoretical studies on NoCs including design methodology topology exploration quality of service guarantee low power design and implementation trials The Steps to Implement NoC The book covers the full spectrum of the subject from theory to actual chip design using NoC Employing the Unified Modeling Language UML throughout it presents complicated concepts such as models of computation and communication computation partitioning in a manner accessible to laypeople The authors provide guidelines on how to simplify complex networking theory to design a working chip In addition they explore the novel NoC techniques and implementations of the Basic On Chip Network BONE project Examples of real time decisions circuit level design systems and chips give the material a real world context Low Power NoC and Its Application to SoC Design Emphasizing the application of NoC to SoC design this book shows how to build the complicated interconnections on SoC while keeping a low power consumption *Electronic Engineering and Computing Technology* Len Gelman,2010-04-21 Electronic Engineering and Computing Technology contains sixty one revised and extended research articles written by prominent researchers participating in the conference Topics covered include Control Engineering Network Management Wireless Networks Biotechnology Signal Processing Computational Intelligence Computational Statistics Internet Computing High Performance Computing and industrial applications Electronic Engineering and Computing Technology will offer the state of art of tremendous advances in electronic engineering and computing technology and also serve as an excellent reference work for researchers and graduate students working with on electronic engineering and computing technology **Electronic Design Automation for IC System Design, Verification, and Testing** Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE

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16th International Conference on Information Technology-New Generations (ITNG 2019) Shahram Latifi, 2019-05-22 This 16th International Conference on Information Technology New Generations ITNG continues an annual event focusing on state of the art technologies pertaining to digital information and communications The applications of advanced information technology to such domains as astronomy biology education geosciences security and health care are among topics of relevance to ITNG Visionary ideas theoretical and experimental results as well as prototypes designs and tools that help the information readily flow to the user are of special interest Machine Learning Robotics High Performance Computing and Innovative Methods of Computing are examples of related topics The conference features keynote speakers the best student award poster award service award a technical open panel and workshops exhibits from industry government and academia

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