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Term End Examination - May 2013

Course : CSE109 - Digital Logic   Slot: C2

Class NBR : 4109 / 4111 / 4112 / 4113 / 4116

Time : Three Hours   Max.Marks:100

PART – A (8 X 5 = 40 Marks)
Answer ALL Questions

1. a) What is the base 4 equivalent of the Hex Number 3A7?   [2+3]

    b) Show the value of all bits of a 12-bit register that hold the number equivalent to

    decimal 215 in (i) binary; (ii) octal; (iii) hexadecimal; (iv) binary-coded decimal (BCD).

2. Given the Boolean expression $F = x'y + xyz'$:

    a) Derive an algebraic expression for the complement F'.

    b) Show that $F \cdot F' = 0$ and $F + F' = 1$

3. Implement the four Boolean functions listed using three half-adder circuits. Use only block diagrams of half adder to show connections.

    $$D = A \oplus B \oplus C$$

    $$E = A'BC + AB'C$$

    $$F = ABC' + (A' + B') C$$

    $$G = ABC$$

4. We want to design a combinational circuit that computes the function $f(x) = x^2 + x + 2$ for a 2-bit x:

    How many bits do we need for the output?

    Derive the truth table and draw the logic circuit using NAND.

5. Explain BCD Ripple counter with a neat diagram.

6. The operation performed in an ALU is $F = A + B'$. Derive a table for the six relationships in terms of the status bit conditions E and C defined above.

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Brendan G. Carr

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