

CHAPTER 2

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2-1.*

a) $\overline{XYZ} = \overline{X} + \overline{Y} + \overline{Z}$

Verification of DeMorgan's Theorem

X	Y	Z	XYZ	\overline{XYZ}	$\overline{X} + \overline{Y} + \overline{Z}$
0	0	0	0	1	1
0	0	1	0	1	1
0	1	0	0	1	1
0	1	1	0	1	1
1	0	0	0	1	1
1	0	1	0	1	1
1	1	0	0	1	1
1	1	1	1	0	0

b) $\overline{X + YZ} = (\overline{X + Y})(\overline{X + Z})$

The Second Distributive Law

X	Y	Z	YZ	$\overline{X + YZ}$	$\overline{X + Y}$	$\overline{X + Z}$	$(\overline{X + Y})(\overline{X + Z})$
0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0
0	1	0	0	0	1	0	0
0	1	1	1	1	1	1	1
1	0	0	0	1	1	1	1
1	0	1	0	1	1	1	1
1	1	0	0	1	1	1	1
1	1	1	1	1	1	1	1

c) $\overline{XY} + \overline{YZ} + \overline{XZ} = \overline{XY + YZ + XZ}$

X	Y	Z	\overline{XY}	\overline{YZ}	\overline{XZ}	$\overline{XY + YZ + XZ}$	\overline{XY}	\overline{YZ}	\overline{XZ}	$\overline{XY + YZ + XZ}$
0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	1	0	1	0	0	1	1
0	1	0	1	0	0	1	0	1	0	1
0	1	1	1	0	0	1	0	0	1	1
1	0	0	0	0	1	1	1	0	0	1
1	0	1	0	1	0	1	1	0	0	1
1	1	0	0	0	1	1	0	1	0	1
1	1	1	0	0	0	0	0	0	0	0

2-2.*

a) $\overline{X}\overline{Y} + \overline{X}Y + X\overline{Y} = \overline{X} + \overline{Y}$
 $= (\overline{X}\overline{Y} + \overline{X}Y) + (\overline{X}\overline{Y} + X\overline{Y})$
 $= \overline{X}(\overline{Y} + Y) + \overline{Y}(\overline{X} + X)$
 $= \overline{X} + \overline{Y}$

b) $\overline{A}B + \overline{B}\overline{C} + AB + \overline{B}C = 1$
 $= (\overline{A}B + AB) + (\overline{B}\overline{C} + \overline{B}C)$
 $= B(A + \overline{A}) + \overline{B}(C + \overline{C})$

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DP Hallahan



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Logic and Computer Design Fundamentals Mr. Rohit Manglik, 2024-07-11 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels

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Digital System Design Using VHDL Rishabh Anand,2023 The book covers the complete syllabus of subject as suggested by most of the universities in India Generic VHDL code is taught and used through out the book so that different companies VHDL tools can be used if desired Moving from the unknown in a logical manner Subject matter in each chapter develops systematically from inceptions Large number of carefully selected worked examples in sufficient details No other reference is required Ideally suited for self study

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This text is designed to provide hands on experience to students to help them develop an understanding of the hardware components of a microprocessor and the role of software in programming and interfacing aspects of the microprocessor An 8 bit microprocessor due to its simpler instruction set and architecture is an ideal IC chip for providing the students with a solid foundation for micro processors their principles and applications The concepts of all state of the art processors can be understood easily once the basics of the 8085 are understood Today s sophisticated microprocessors have a semblance of 8085 The presentation style adopted in this book in a way is unique It is a student friendly text written as conversation between the teacher and the students The book lucidly explains the various programming examples in assembly language with a view to enabling students to develop microprocessor based industrial application projects Application programs developed in the book are based on the popular microprocessor kit namely SDA 85 The book is suitable for both diploma and degree level students pursuing courses in Electronics and Electrical Engineering Electronics and Communication Engineering and Information and Communication Technology

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