



Seat No.	
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S.Y. B.Sc. (Computer Science) (Semester – II) Examination, 2013
ELC – 221 : ELECTRONICS (Paper – I)
Microcontroller and Embedded Systems
(2008 Pattern)

Time : 2 Hours

Max. Marks : 40

Instructions : 1) *All questions are compulsory.*
2) *Figures to the right indicate full marks.*
3) *Neat diagrams must be drawn wherever necessary.*

1. Answer the following in **one** or **two** sentences : **(1×10=10)**

- a) What is the address stored in stack pointer and program counter after 8051 is reset ?
- b) Which address of internal RAM is used for bit addressing ?
- c) Which port pins are used for accessing external interrupts ?
- d) What will be the effect of instruction
back : JNB P1.5, back
end.
- e) If T_{H1} register in Mode 2 operation is loaded with – 50, then after how many clock pulses the timer overflow flag will be set ?
- f) Give the vector address for Timer-0 and serial communication interrupts.
- g) Mention the role of line drivers in serial communication.
- h) Define Emulator.
- i) Define IDE.
- j) Give the standard reference current for DAC 0808.

Embedded Systems Question Paper For Eie

RJ Shavelson



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Reviewing **Embedded Systems Question Paper For Eie**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is truly astonishing. Within the pages of "**Embedded Systems Question Paper For Eie**," an enthralling opus penned by a very acclaimed wordsmith, readers embark on an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve into the book's central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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