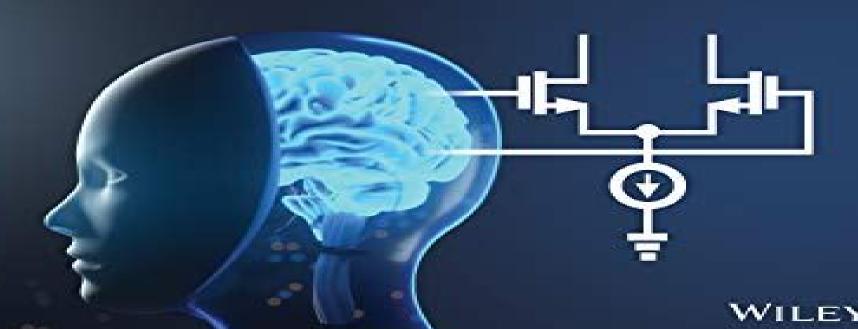
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Nanoelectronic Devices for Hardware and Software Security Arun Kumar Singh, Balwinder Raj, 2021-10-31 Nanoelectronic Devices for Hardware and Software Security has comprehensive coverage of the principles basic concepts structure modeling practices and circuit applications of nanoelectronics in hardware software security It also covers the future research directions in this domain In this evolving era

nanotechnology is converting semiconductor devices dimensions from micron technology to nanotechnology Nanoelectronics would be the key enabler for innovation in nanoscale devices circuits and systems The motive for this research book is to provide relevant theoretical frameworks that include device physics modeling circuit design and the latest developments in experimental fabrication in the field of nanotechnology for hardware software security There are numerous challenges in the development of models for nanoscale devices e q FinFET gate all around devices TFET etc short channel effects fringing effects high leakage current and power dissipation among others This book will help to identify areas where there are challenges and apply nanodevice and circuit techniques to address hardware software security issues **Phase-Locked Loops** Behzad Razavi, 2020-01-30 This modern pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design featuring intuitive presentation of theoretical concepts extensive circuit simulations over 200 worked examples and 250 end of chapter problems The perfect text for senior undergraduate and graduate students Machine Learning-based Design and Optimization of High-Speed Circuits Vazgen Melikyan, 2023-12-30 This book describes machine learning based new principles methods of design and optimization of high speed integrated circuits included in one electronic system which can exchange information between each other up to 128 256 512 Gbps speed The efficiency of methods has been proven and is described on the examples of practical designs This will enable readers to use them in similar electronic system designs The author demonstrates newly developed principles and methods to accelerate communication between ICs working in non standard operating conditions considering signal deviation compensation with linearity self calibration. The observed circuit types also include but are not limited to mixed signal high performance heterogeneous integrated circuits as well as digital cores **Enabling 6G Mobile** Networks Jonathan Rodriguez, Christos Verikoukis, John S. Vardakas, Nikos Passas, 2021-11-05 This book tackles the 6G odyssey providing a concerted technology roadmap towards the 6G vision focused on the interoperability between the wireless and optical domain including the benefits that are introduced through virtualization and software defined radio The authors aim to be at the forefront of beyond 5G technologies by reflecting the integrated works of several major European collaborative projects H2020 ETN SECRET 5GSTEPFWD and SPOTLIGHT The book is structured so as to provide insights towards the 6G horizon reporting on the most recent developments on the international 6G research effort The authors address a variety of telecom stakeholders which includes practicing engineers on the field developing commercial solutions for 5G and beyond products postgraduate researchers that require a basis on which to build their research by highlighting the current challenges on radio optical and cloud based networking for ultra dense networks including novel approaches and project managers that could use the principles and applications for shaping new research proposals on this highly dynamic field Embedded Systems Design for High-Speed Data Acquisition and Control Maurizio Di Paolo Emilio, 2014-09-01 This book serves as a practical guide for practicing engineers who need to design embedded systems for high speed data

acquisition and control systems A minimum amount of theory is presented along with a review of analog and digital electronics followed by detailed explanations of essential topics in hardware design and software development. The discussion of hardware focuses on microcontroller design ARM microcontrollers and FPGAs techniques of embedded design high speed data acquisition DAQ and control systems Coverage of software development includes main programming techniques culminating in the study of real time operating systems All concepts are introduced in a manner to be highly accessible to practicing engineers and lead to the practical implementation of an embedded board that can be used in various industrial fields as a control system and high speed data acquisition system

Implantable Bioelectronics Evgeny Katz,2014-02-27

Here the renowned editor Evgeny Katz has chosen contributions that cover a wide range of examples and issues in implantable bioelectronics resulting in an excellent overview of the topic The various implants covered include biosensoric and prosthetic devices as well as neural and brain implants while ethical issues suitable materials biocompatibility and energy harvesting devices are also discussed A must have for both newcomers and established researchers in this interdisciplinary field that connects scientists from chemistry material science biology medicine and electrical engineering

State-of-the-Art of Millimeter-Wave Silicon Technology Jaco du Preez, Saurabh Sinha, 2022-09-20 This book examines the critical differences between current and next generation Si technologies CMOS BiCMOS and SiC and technology platforms e g system on chip in mm wave wireless applications. We provide a basic overview of the two technologies from a technical standpoint followed by a review of the state of the art of several key building blocks in wireless systems The influences of system requirements on the choice of semiconductor technology are vital to understanding the merits of CMOS and BiCMOS devices e g output power battery life adjacent channel interference cost restrictions and so forth These requirements in turn affect component level design and performance metrics of oscillators mixers power and low noise amplifiers as well as phase locked loops and data converters Finally the book offers a peek into the next generation of wireless technologies such as THz band systems and future 6G applications Smart Small Satellites: Design, Modelling and Development Chander Prakash, V. Sambasiva Rao, D. V. A. Raghava Murthy, 2023-01-02 This book comprises the select proceedings of the International Conference on Small Satellites and its Applications ICSS 2022 It aims to provide a comprehensive and broad spectrum picture of the state of the art research development and commercial perspective of various discoveries conducted in the real world smart small satellites applications and their services. The contents of this book focuses on efficient power management system application based optimum payload designs telemetry and telecommand advanced navigation and RF systems flight and ground software s structure mechanism and materials space craft autonomy quality testing and reliability for designing the small satellites through advanced computational procedures for a variety of applications etc This book proves a valuable resource for those in academia and industry CMOS Receiver Front-ends for Gigabit Short-Range Optical Communications Francisco Aznar, Santiago Celma Pueyo, Belén Calvo Lopez, 2012-08-09 This

book describes optical receiver solutions integrated in standard CMOS technology attaining high speed short range transmission within cost effective constraints These techniques support short reach applications such as local area networks fiber to the home and multimedia systems in cars and homes The authors show how to implement the optical front end in the same technology as the subsequent digital circuitry leading to integration of the entire receiver system in the same chip The presentation focuses on CMOS receiver design targeting gigabit transmission along a low cost standardized plastic optical fiber up to 50m in length This book includes a detailed study of CMOS optical receiver design from building blocks to the system level

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