I-Type Instructions

- The operation is determined by the opcode, highlighted in blue. The operands are specified in the three fields rs, rt, and imm.
 - rs and imm are always used as source operands.
 - rt is used as a destination for some instructions (such as addi and lw) but as another source for others (such as sw).
- Figure 6.9 shows several examples of encoding I-type instructions.
 - Recall that negative immediate values are represented using 16-bit two's complement notation.
 - rt is listed first in the assembly language instruction when it is used as a destination, but it is the second register field in the machine language instruction.

Imm 00instruction Guide

David A. Patterson, John L. Hennessy

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