- Byte 2 has three fields
- Mode field (MOD)
- Register field (REG) used to identify the register for the first operand
- Register/memory field (R/M field)

REG	W = 0	W = 1	
000	AL	AX	
001	CL	сх	
010	DL	DX	
011	BL	BX	
100	AH	SP	
101	СН	BP BP	
110	DH	SI	
111	ВН	DI	

opcode D W	MOD REG	R/M
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Marco Cascella

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Computerworld ,1982-06-28 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld s award winning Web site Computerworld com twice monthly publication focused conference series and custom research form the hub of the world s largest global IT media network 8086/8088 User's Manual Intel Corporation,1989 An Analysis of Instruction Set Usage of the Intel 8086/88

Microprocessor 8086: Architecture, Programming and Interfacing Mathur Microprocessor Thomas LeRoy Adams, 1988 Sunil,2010-12 Primarily intended for the undergraduate students of electronics and communication engineering computer science and engineering and information technology this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor It offers the students an up to date account of the state of the art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips The book covers the advanced microprocessor architecture of the Intel microprocessor family from 8086 to Pentium 4 The text is organized in four parts Part I Chapters 1 7 includes a detailed description of the architecture organization instruction set and assembler directives of microprocessor 8086 Part II Chapters 8 11 discusses the math coprocessor multiprocessing and multiprogramming the different types of data transfer schemes and memory concepts Part III Chapters 12 15 covers programmable interfacing chips with the help of extensive interfacing examples Part IV Chapters 16 18 deals with advanced processors from 80186 to Pentium 4 This well organized and student friendly text should prone to be an invaluable asset to the students as well as the practising engineers KEY FEATURES Gives elaborate programming examples to develop the analytical ability of students Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students Furnishes chapter end exercises to reinforce the understanding of the subject The 8086 and 80286 Microprocessors Avtar Singh, Walter A. Triebel, 1990 **The 8086 Primer** Stephen P. Morse, 1980 The 8088 and 8086 Microprocessors Walter A. Triebel, Avtar Singh, 2003 Introduction to microprocessors and microcomputers Software architecture of the 8088 and 8086 microprocessors Assembly language programming Machine language coding and the debug software development program of IBM PC 8088 8086 programming integer instructions and computations 8088 8086 programming control flow instructions and program structures Assembly language program development with masm The 8088 and 8086 microprocessors and their memory and input output interfaces Memory devices circuits and subsystem design Input output interface circuits and LSI peripheral devices Interrupt interface of the 8088 and 8086 microprocessors Hardware of the original IBM PC microcomputer PC bus interfacing circuit construction testing and troubleshooting Real mode software and hardware architecture of the 80286 microprocessor The 80386 80486 and pentium processor families software architecture The 80386 80486 and pentium processor families hardware architectu The 8086 Microprocessor Walter A. Triebel, Avtar Singh, 1985 Discusses the Architecture Characteristics of the 8086 Chip Details Programming

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Summary Appendix C Flag Bit Changes Appendix D Answers to Selected Even Numbered Questions and Problems Index The 8088 and 8086 Microprocessors: Programming, Interfacing, Software, Hardware, and Applications, 4e Triebel, Future designers of microprocessor based electronic equipment require a systems level understanding of the 80x86 microcomputer This widely acclaimed edition provides balanced and comprehensive coverage of both the software and hardware of the 8088 and 8086 microprocessors. The book examines how to assemble run and debug programs and how to build test and troubleshoot interface circuits New material has been added on number system conversations binary arithmetic and combinational logic operations i386/i486 Advanced Programming Sen-cuo Ro,2012-03-08 This book gives x86 assembly language programmers a view about how to use the resources and features provided by the i386 i486 processor the newest and most advanced microprocessor from the Intel x86 family Because the i386 i486 processor is entirely compatible with its predecessor the 8086 88 processor this book concentrates on the enhanced features compared to its predecessor. We assume the reader is already familiar with the concepts of 8086.88 assembly language programming. Our goal is to show you the programming methods that apply to powerful features of the i386 i486 The i387 math coprocessor is not discussed in this book A detailed explanation about how to use each i386 i486 instruction is not covered in this book However we list the complete i386 i486 instruction set in Appendix B Organization of the Book This book is divided into sections to help readers start learning from the concepts that are similar to the 8086 8088 processor Then the discussion shifts to the resources and environment of the i386 i486 processor Throughout the book real life program examples are used to illustrate in detail how you can use the enhanced features or functions of the processor Chapter 1 introduces the i386 i486 architecture and its enhanced features The discussion includes the operation mode general registers segment registers system registers and system data structures Chapter 2 discusses the method that the i386 i486 processor uses to make itself fully compatible with the 8086 88 processor and to define the interrupt vector table address which is different from the 8086 88 processor An Introduction to 8086/8088 Assembly Language Programming Thomas P. Skinner, 1985 This hands on guide helps develop programming skills on the 8086 based microcomputers Introduces readers to assembly language programming through a comprehensive set of input output procedures and useful subroutines for the most popular 8086 based operating systems Covering fundamental data types segmentation assembler operation and modular programming these routines let users apply assembly language shortcuts and programming techniques to specific applications Offers a brief outline of the design of the l6 bit microprocessor and the architecture of the 8086 including the 80286 family of chips presents the essentials on binary and hexadecimal numbers and shows how to write and execute a program The complete instruction set is presented in the last nine chapters **The 80386/387 Architecture** Stephen P. Morse, Douglas J. Albert, Eric J. Isaacson, 1987-09-09 A comprehensive guide to the hottest new chip to the hottest new microprocessor available describing machine organization memory structure and I O and register design Illustrated **ASM86 Language**

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