AGJ 1st half (r)con-code 878 Con. 6634-13. 31/05/13

GS-7302

10

10

(3 Hours)

[Total Marks : 100

| N. B. | :(1) | Question No. 1 is Compulsory. | |
|-------|-------|--|---------------|
| | (2) | Solve any four questions from the remaining. | |
| | (3) | Assume suitabale data wherever necessary and justify the same. | |
| 1. | | | 20 |
| | (a) | Draw the V-I characteristics of SCR with and without gate control and mark holding current and latching current? Explain the difference between holding current and latching current | |
| | (b) | Explain why cyclo converters are suitable only for low frequency application? | |
| | (c) | Explain the meaning of following instructions a) LDA address b)MVI M,12H | |
| | (d) | Realize EX-OR gate by NAND gate | |
| | (c) | Why RC triggering is preferred over Resistance triggering process? | |
| 2. | (a) | Explain single phase full wave fully controlled bridge rectifier with the help of circuit diagram and waveforms. | 10 |
| | (p) | Explain the UJT relaxation circuit to trigger SCR along with appropriate waveform. | 10 |
| 3- | (a) | Explain with block diagram the armsture control method for speed control of a DC shunt motor. | 10 |
| | (b) | What is the difference between Natural and Forced commutation? Explain any two forced commutation techniques of SCR with waveforms. | 10 |
| 4. | (a) | Explain any two circuits using SCR for the speed control of single phase induction motor. | 10 |
| | (b) | Draw the circuit diagram and explain the working of Jones chopper | 10 |
| 5. | (a) | Discuss the thyristerised over load and over voltage protection of DC Motor. | 10 |
| | (b) | Explain important characteristics of OPAMP | 05 |
| | (c) | Draw a neat schematic diagram for OP-AMP in inverting and non-inverting mode | 0.5 |
| 6. | (a) | Which gates are called as Universal gates? Why? Using K-map reduce following Boolean function and implement it using NAND gates | 10 |
| | 49.76 | $f(A,B,C)=\sum m(0,1,3,4,5,6)$ | Total Control |
| | (b) | | 0.5 |
| | (c) | Explain DeMorgan's theorem. | 0.5 |
| | | | |

(a) Explain the architecture of 8085 Microprocessor with block diagram.

(b) Explain 555 timer as a stable multivibrator

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