#### Lab No. 01

# Objective:

- Introduction to MP lab and PIC microcontroller
- Creating a file

#### Introduction:

In this lab, we will learn how to open the MP-LAB software and open the file for programming. Basically, it is the introduction lab for the semester work in Embedded lab. This introduction lab is very useful for coming projects and work in lab.

# Theory:

#### MP-Lab:

MP-lab is a Public domain software and is developed for the embedded applications on PIC and microcontroller and this is developed by multinational company named as Microchip technology. MP lab supports the coding, debugging and programming of microchips, microcontrollers of 8bit, 16bits and DS-PIC microcontroller and also for 32bit software asset management and Pic-Microcontrollers 32 bits.



Figure 1.1 MP-Lab main screen

#### PIC-Microcontroller:

The word PIC stands for Peripheral Interface Controller. Firstly, PIC developed for the PDP Program Data processor computers for controlling the Peripheral devices. These are very easy

# **Embedded Systems Lab Manual Objective**

**Gnanasivam** 

# **Embedded Systems Lab Manual Objective:**

**Real-Time Embedded Systems** Xiaocong Fan, 2015-02-25 This book integrates new ideas and topics from real time systems embedded systems and software engineering to give a complete picture of the whole process of developing software for real time embedded applications You will not only gain a thorough understanding of concepts related to microprocessors interrupts and system boot process appreciating the importance of real time modeling and scheduling but you will also learn software engineering practices such as model documentation model analysis design patterns and standard conformance This book is split into four parts to help you learn the key concept of embedded systems Part one introduces the development process and includes two chapters on microprocessors and interrupts fundamental topics for software engineers Part two is dedicated to modeling techniques for real time systems Part three looks at the design of software architectures and Part four covers software implementations with a focus on POSIX compliant operating systems With this book you will learn The pros and cons of different architectures for embedded systems POSIX real time extensions and how to develop POSIX compliant real time applications How to use real time UML to document system designs with timing constraintsThe challenges and concepts related to cross development Multitasking design and inter task communication techniques shared memory objects message queues pipes signals How to use kernel objects e g Semaphores Mutex Condition variables to address resource sharing issues in RTOS applications The philosophy underpinning the notion of resource manager and how to implement a virtual file system using a resource manager The key principles of real time scheduling and several key algorithms Coverage of the latest UML standard UML 2 4 Over 20 design patterns which represent the best practices for reuse in a wide range of real time embedded systems Example codes which have been tested in QNX a real time operating system widely adopted in Applied Biomechanics Lab Manual John C. Garner, Charles Allen, Adam C. Knight, Harish Chander, 2022-07-12 industry Applied Biomechanics Laboratory Manual offers 13 easy to follow experiential based learning labs offering students conceptual understanding of biomechanics to practical applications Microprocessor (8085) Lab Manual G.T. A Practical Guide to Basic Laboratory Andrology David Mortimer, Lars Björndahl, C. L. R. Barratt, José Swamy, 2006 Antonio Castilla, Roelof Menkveld, 2022-03-10 Preceded by A practical guide to basic labratory andrology Lars Bj orndahl et al 2010 **UML-B Specification for Proven Embedded Systems Design** Jean Mermet, 2013-03-19 This book presents the perspective of the project on a Paradigm Unifying System Specification Environments for proven Electronic design PUS SEE as conceived in the course of the research during 2002 2003 The initial statement of the research was formulated as follows The objective of PUSSEE is to introduce the formal proof of system properties throughout a modular system design methodology that integrates sub systems co verification with system refinement and reusability of virtual system components This will be done by combining the UML and B languages to allow the verification of system specifications through the composition of proven sub systems in particular interfaces using the VSIAISLIF standard The link of B with C VHDL and

SystemC will extend the correct by construction design process to lower system on chip SoC development stages Prototype tools will be developed for the code generation from UML and B and existing B verification tools will be extended to supportIP reuse according to the VSI Alliance work The methodology and tools will be validated through the development of three industrial applications a wireless mobile terminal a telecom system on chip based on HIPERLANI2 protocol and an anti collision module for automobiles The problem was known to be hard and the scope ambitious But the seventeen chapters that follow describing the main results obtained demonstrate the success of the research acknowledged by the European reviewers They are released to allow the largest audience to learn and take benefit of Scientific and Technical Aerospace Embedded Systems LAB Manual L Malathi, 2021-05-06 The Embedded Systems Laboratory Manual is having the laboratory experiments related to embedded systems It will useful for Electronics and Communication Engineering also other engineering program and courses The core design of this book concentrated for laboratory aspect All the modules includes Analysis Discussion and Conclusion part Which will give the good practical knowledge to the students in different observations of the individual experiment Many interfacing concepts have dealt for embedded systems like ADC DAC LCD LED and PWM Stepper Motor Temperature Sensor Keyboard EPROM and Interrupt Real Time Clock and Serial Part - Anatomy & Physiology Laboratory Manual - E-Book Kevin T Patton, PhD, 2014-12-02 Effectively master various Port physiology dissection identification and anatomic explorations in the laboratory setting with the Anatomy Physiology Laboratory Manual 9th Edition This practical full color lab manual contains 55 different A P lab exercises that cover labeling anatomy identification dissection physiological experiments computerized experiments and more The manual also includes safety tips a comprehensive instruction and preparation guide for the laboratory and tear out worksheets for each of the 55 exercises In addition 8 e Lab modules offer authentic 3D lab experiences online for virtual lab instruction 8 interactive eLabs further your laboratory experience in the digital environment Complete list of materials for each exercise offers a thorough checklist for planning and setting up laboratory activities Over 250 illustrations depict proper procedures and common histology slides Step by step guidance for dissection of anatomical models and fresh or preserved specimens with accompanying illustrations helps you become acclimated to the lab environment Physiology experiments centering on functional processes of the human body offer immediate and exciting examples of physiological concepts Easy to evaluate tear out lab reports contain checklists drawing exercises and questions that help you demonstrate your understanding of the labs they have participated in Reader friendly spiral binding allows for hands free viewing in the lab setting Labeling and coloring exercises provide opportunities to identify critical structures examined in the lab and lectures Brief learning aids such as Hints Landmark Characteristics and Safety First are found throughout the manual to help reinforce and apply knowledge of anatomy and function Modern anatomical imaging techniques such as MRIs CTs and ultrasonography are introduced where appropriate Boxed hints and safety tips provide you with special insights on handling specimens using

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