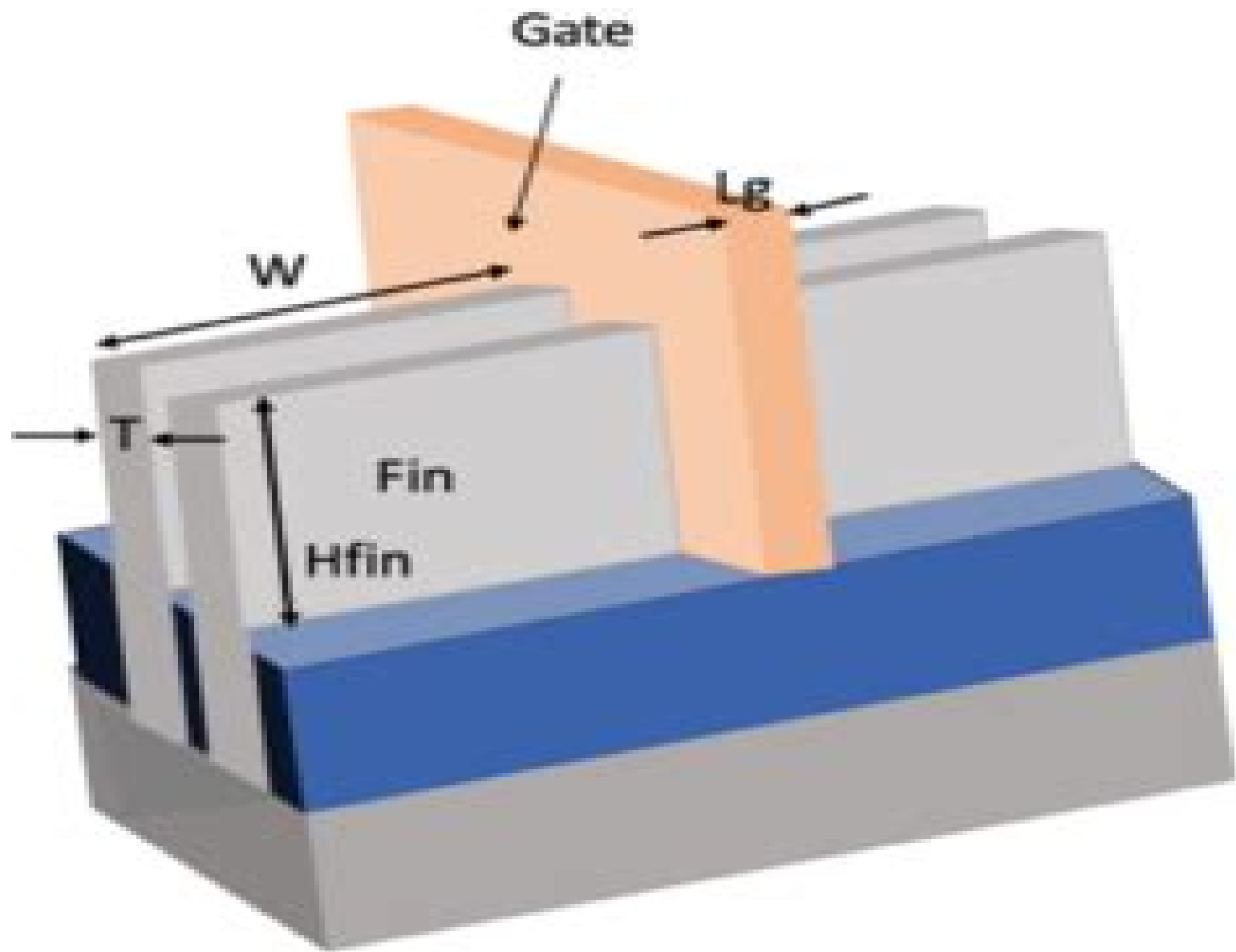




Critical Dimensions of FinFET

Finfet Design Manual

**Brajesh Kumar Kaushik, Sudeb
Dasgupta, Virendra Singh**



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FinFET Modeling for IC Simulation and Design Yogesh Singh Chauhan, Darsen Lu, Sriramkumar

Vanugopalan, Sourabh Khandelwal, Juan Pablo Duarte, Navid Payvadosi, Ali Niknejad, Chenming Hu, 2015-03-17 This book is the first to explain FinFET modeling for IC simulation and the industry standard BSIM CMG describing the rush in demand for advancing the technology from planar to 3D architecture as now enabled by the approved industry standard The book gives a strong foundation on the physics and operation of FinFET details aspects of the BSIM CMG model such as surface potential charge and current calculations and includes a dedicated chapter on parameter extraction procedures providing a step by step approach for the efficient extraction of model parameters With this book you will learn Why you should use FinFET The physics and operation of FinFET Details of the FinFET standard model BSIM CMG Parameter extraction in BSIM CMG FinFET circuit design and simulation Authored by the lead inventor and developer of FinFET and developers of the BSIM CM standard model providing an experts insight into the specifications of the standard The first book on the industry standard FinFET model BSIM CMG

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Hu, S. Salahuddin, Girish Pahwa, Avirup Dasgupta, Darsen Lu, Sriramkumar Vanugopalan, Ali Niknejad, Sourabh Khandelwal, Juan Pablo Duarte, Navid Payvadosi, 2024-08-23 FinFET GAA Modeling for IC Simulation and Design Using the BSIM CMG Standard Second Edition is the first to book to explain FinFET modeling for IC simulation and the industry standard BSIM CMG describing the rush in demand for advancing the technology from planar to 3D architecture as now enabled by the approved industry standard The book gives a strong foundation on the physics and operation of FinFET details aspects of the BSIM CMG model such as surface potential charge and current calculations and includes a dedicated chapter on parameter extraction procedures thus providing a step by step approach for the efficient extraction of model parameters With this book users will learn Why you should use FinFET The physics and operation of FinFET Details of the FinFET standard model BSIM CMG Parameter extraction in BSIM CMG FinFET circuit design and simulation and more Authored by the lead inventor and developer of FinFET and developers of the BSIM CMG standard model providing an expert s insight into the specifications of the standard A new edition of the original groundbreaking book on the industry standard FinFET model BSIM CMG New to This Edition Includes a new chapter providing a comprehensive introduction to GAAFET including motivations device concepts structure benefits and the industry standard GAAFET model Covers the most recent developments in the BSIM CMG model Presents an updated RF modeling of FinFET using the BSIM CMG model including parameter extraction Includes a new chapter on cryogenic modeling

FinFET Devices for VLSI Circuits and Systems

Samar K. Saha, 2020-07-15 To surmount the continuous scaling challenges of MOSFET devices FinFETs have emerged as the real alternative for use as the next generation device for IC fabrication technology The objective of this book is to provide the basic theory and operating principles of FinFET devices and technology an overview of FinFET device architecture and

manufacturing processes and detailed formulation of FinFET electrostatic and dynamic device characteristics for IC design and manufacturing Thus this book caters to practicing engineers transitioning to FinFET technology and prepares the next generation of device engineers and academic experts on mainstream device technology at the nanometer nodes

Mm-wave Circuit Design in 16nm FinFET for 6G Applications Bart Philippe,Patrick Reynaert,2022-09-24 This book tackles the challenges of designing mm wave circuits in 16nm FinFET from the elementary transistor level to a measured D band transmitter The design of crucial building blocks such as oscillators and power amplifiers are covered through theoretical limitations design methodology and measurement Offers first book on design of mm wave circuits above 100GHz in an advanced 16nm FinFET digital technology Covers fundamentals of transistor layout circuit implementation and measurements Provides single source reference to information otherwise only available in disparate literature

Handbook of Computer Networks and Cyber Security Brij B. Gupta,Gregorio Martinez Perez,Dharma P. Agrawal,Deepak Gupta,2019-12-31 This handbook introduces the basic principles and fundamentals of cyber security towards establishing an understanding of how to protect computers from hackers and adversaries The highly informative subject matter of this handbook includes various concepts models and terminologies along with examples and illustrations to demonstrate substantial technical details of the field It motivates the readers to exercise better protection and defense mechanisms to deal with attackers and mitigate the situation This handbook also outlines some of the exciting areas of future research where the existing approaches can be implemented Exponential increase in the use of computers as a means of storing and retrieving security intensive information requires placement of adequate security measures to safeguard the entire computing and communication scenario With the advent of Internet and its underlying technologies information security aspects are becoming a prime concern towards protecting the networks and the cyber ecosystem from variety of threats which is illustrated in this handbook This handbook primarily targets professionals in security privacy and trust to use and improve the reliability of businesses in a distributed manner as well as computer scientists and software developers who are seeking to carry out research and develop software in information and cyber security Researchers and advanced level students in computer science will also benefit from this reference

Catching Up and Leapfrogging Xiao-Shan Yap,Rajah Rasiah,2016-12-01 Ever since Schumpeter s groundbreaking work there has been a plethora of new research seeking to extend the direction and dynamics of innovation Using a rich account of detailed interviews this book offers new evidence on how latecomers have successfully caught up and leapfrogged incumbent firms Catching Up and Leapfrogging the new latecomers in the integrated circuits industry explores how technological transitions affect latecomer catch up strategies and vice versa in a high technology industry It looks to the East Asian latecomers who towards the end of the twentieth century pioneered a new pathway through organizational change by specializing in the key production stages of integrated circuits and pushing technologies further This volume assesses how latecomer resource acquisition strategies

have varied alongside structural industry changes and evaluates the mechanisms through which firms started life as technology followers and rose to become technology leaders Xiao Shan Yap and Rajah Rasiah present a unique story about how firm strategies evolve from the catching up phase to the leapfrogging phase captured from the accounts of managers on the ground It is the first time firm level strategies have been systematically analysed to describe twenty first century strategic management in the integrated circuits industry in particular and the high tech industry in general The evidence and analysis in this book offers insights for chief executive officers policy makers and researchers to revisit existing approaches to the theory of catching up and leapfrogging *Nanoelectronic Circuit Design* Niraj K. Jha, Deming Chen, 2010-12-21 This book is about large scale electronic circuits design driven by nanotechnology where nanotechnology is broadly defined as building circuits using nanoscale devices that are either implemented with nanomaterials e g nanotubes or nanowires or following an unconventional method e g FinFET or III V compound based devices These nanoscale devices have significant potential to revolutionize the fabrication and integration of electronic systems and scale beyond the perceived scaling limitations of traditional CMOS While innovations in nanotechnology originate at the individual device level realizing the true impact of electronic systems demands that these device level capabilities be translated into system level benefits This is the first book to focus on nanoscale circuits and their design issues bridging the existing gap between nanodevice research and nanosystem design VLSI Design and Test Manoj Singh Gaur, Mark Zwolinski, Vijay Laxmi, D. Boolchandani, Virendra Sing, Adit Singh, 2013-12-13 This book constitutes the refereed proceedings of the 17th International Symposium on VLSI Design and Test VDAT 2013 held in Jaipur India in July 2013 The 44 papers presented were carefully reviewed and selected from 162 submissions The papers discuss the frontiers of design and test of VLSI components circuits and systems They are organized in topical sections on VLSI design testing and verification embedded systems emerging technology Handbook of Integrated Circuit Industry Yangyuan Wang, Min-Hwa Chi, Jesse Jen-Chung Lou, Chun-Zhang Chen, 2023-11-27 Written by hundreds experts who have made contributions to both enterprise and academics research these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits and cover topics related to the technology evolution trends fabrication applications new materials equipment economy investment and industrial developments of integrated circuits Especially the coverage is broad in scope and deep enough for all kind of readers being interested in integrated circuit industry Remarkable data collection update marketing evaluation enough working knowledge of integrated circuit fabrication clear and accessible category of integrated circuit products and good equipment insight explanation etc can make general readers build up a clear overview about the whole integrated circuit industry This encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field In addition this book provides enough guide lines and knowledges to benefit enterprisers being interested in integrated circuit industry **VLSI Design and Test** Brajesh Kumar Kaushik, Sudeb Dasgupta, Virendra

Singh,2017-12-21 This book constitutes the refereed proceedings of the 21st International Symposium on VLSI Design and Test VDAT 2017 held in Roorkee India in June July 2017 The 48 full papers presented together with 27 short papers were carefully reviewed and selected from 246 submissions The papers were organized in topical sections named digital design analog mixed signal VLSI testing devices and technology VLSI architectures emerging technologies and memory system design low power design and test RF circuits architecture and CAD and design verification

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