



basic education

Department
Basic Education
REPUBLIC OF SOUTH AFRICA

NATIONAL
SENIOR CERTIFICATE

GRADE 11

PHYSICAL SCIENCES: PHYSICS (P1)

NOVEMBER 2017

MARKS: 100

TIME: 1 hour

This question paper consists of 10 pages, 3 data sheets and 1 answer sheet

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Physical Sciences/P1

10
CAPS – Grade 11

100 Marks (P1)

QUESTION 10 (Start on a new page.)

- 10.1 The circuit below consists of a 4 Ω and 16 Ω resistor connected in parallel and an unknown resistor R_x in series. An ammeter, a high-resistance voltmeter, a closed switch and battery are connected, as shown. The resistance of the battery and wires can be ignored.



The total power dissipated in the parallel part of the circuit is 32 W.

- 10.1.1 Define the term power. (2)
10.1.2 Calculate the effective resistance of the parallel combination. (2)
10.1.3 Calculate the potential difference across the resistors in parallel. (2)
10.1.4 Calculate the current through resistor R_x. (2)

The switch in the circuit is now OPENED.

- 10.1.5 How will the reading on the voltmeter (V) be affected?
Choose from INCREASE, DECREASE or REMAIN THE SAME. (1)
10.1.6 Refer the answer to QUESTION 10.1.5. (2)

- 10.2 A geyser, labeled 2000 W, is used for an average of 5 hours per day. The cost of electricity is 30 cents per kWh.

- 10.2.1 Calculate the energy used by the geyser for 5 hours per day. (2)
10.2.2 Calculate the cost of electricity to operate the geyser for a month with 30 days. (2)

(2)

TOTAL: 100

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JE Gale



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Federal Register ,1990-11 *Resources in Education* ,2001-04 Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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