Question Paper Code : 21266

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2012.

Sixth Semester

Electronics and Communication Engineering

080290040 — MICROCONTROLLERS AND RISC ARCHITECTURE

(Regulation 2008)

Time : Three hours   Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. How does the pin $\overline{EA}$ of 8051 help in distinguishing the address range of program memory?

2. Show the circuit used for power down operation of 8051.

3. What are the data types and operand addressing supported in 8051?

4. The carry flag is originally set. The data present at the input port 3 is C5H. The data previously written to output port 1 is 35H calculate the port 1 contents after the execution of the following instructions.

    MOV P1.3, C

    MOV C, P3.3

    MOV P1.2, C

5. How do you generate the data corresponding to the step angle required in stepped motor interfacing?

6. Find an application that uses the auto - reload mode of timer 2 in 8051. Justify your answer.

7. List the features of ARM architecture that are not shared by most other RISC architectures.

8. Give an example for pre-indexed addressing mode of ARM.

9. Tabulate the arm- exceptions with their vector address.

10. What are the common sources of memory fault allowed by ARM processors?

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Microprocessor and Microcontroller Interview Questions: Anita Gehlot Rajesh Singh,2020-01-01 Crack the Microprocessor and Microcontroller Interview Description Book gives you a complete idea about the Microcontroller and Microprocessor It starts from a very basic concept like a number system then explains the digital circuit This book is a complete set of interview questions and answers with plenty of screenshots Book takes you on a journey to Microprocessor 8085 Peripheral Devices and Interfacing AVR ATmega32 Interfacing of Input Output Device Book also covers the descriptive questions multiple choice questions along with answers which are asked during an interview Key features An ample number of diagrams are used to illustrate the subject matter for easy understanding Set of review questions with answers are added at the end for better understanding Includes basic to advanced interview questions on 8085 8086 89C51 PIC and AVR interfacing of input output devices It will help to enhance the programming skills of the reader What will you learn Basics to an advanced interview question for microprocessor 8085 8086 and microcontroller 89C51 PIC and AVR Question on interfacing of input output devices Who this book is for Engineering students pursuing a course in electrical and electronics electronics and communication computer science and information technology who wish to learn about Microprocessor Microcontroller and crack an interview Table of Contents 1 Number Systems 2 Digital Circuit 3 Microprocessor 8085 4 Peripheral Devices and Interfacing 5 AVR ATmega32 6 Interfacing of Input Output Device 7 Excercise 8 Descriptive Type Questions 9 Multiple Choice Questions **UGC NET Electronic Science Practice Question Asnwer Sets [Question Bank] Unit Wise As Per Updated Syllabus : Include 4000+ Question Answers** DIWAKAR EDUCATION HUB,2021-09-22 UGC NTA NET ELECTRONIC SCIENCE Code 88 4500 Unit Wise Topic Wise Practice Question Answer As Per Updated Syllabus MCQs Highlight 1 Complete Details all Topics Subjects Covered Based on all 10 Units 2 Unit Wise Practice Question and Answer MCQs 450 MCQs of each UNIT Total 4500 MCQs 3 Prepared by Expert Faculty 4 As Per the New Updated Syllabus 5 All Questions With Solutions Explanations For More Details Call in Our Offical Number 7310762592

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Architecture and Design of Distributed Embedded Systems Bernd Kleinjohann,2013-04-18 Due to the decreasing production costs of IT systems applications that had to be realised as expensive PCBs formerly can now be realised as a system on chip Furthermore low cost broadband communication media for wide area communication as well as for the realisation of local distributed systems are available Typically the market requires IT systems that realise a set of specific features for the end user in a given environment so called embedded systems Some examples for such embedded systems are control systems in cars airplanes houses or plants information and communication devices like digital TV mobile phones or autonomous systems like service or edutainment robots For the design of embedded systems the designer has to tackle three major aspects The application itself including the man machine interface The target architecture of the system including all functional and non functional constraints and the design methodology including modelling specification synthesis test and validation The last two points are a major focus of this book This book documents the high quality approaches and results that were presented at the International Workshop on Distributed and Parallel Embedded Systems DIPES 2000 which was sponsored by the International Federation for Information Processing IFIP and organised by IFIP working groups WG10 3 WG10 4 and WG10 5 The workshop took place on October 18 19 2000 in Schlo Eringerfeld near Paderborn Germany Architecture and Design of Distributed Embedded Systems is organised similar to the workshop Chapters 1 and 4 Methodology I and II deal with different modelling and specification paradigms and the corresponding design methodologies Generic system architectures for different classes of embedded systems are presented in Chapter 2 In Chapter 3 several design environments for the support ofspecific design methodologies are presented Problems concerning test and validation are discussed in Chapter 5 The last two chapters include distribution and communication aspects Chapter 6 and synthesis techniques for embedded systems Chapter 7 This book is essential reading for computer science researchers and application developers **Safety of Computer Control Systems 1992 (SAFECOMP' 92)** H.H. Frey,2014-05-23 SAFECOMP 92 advances the state of the art reviews experiences of the past years considers the guidance now available and identifies the skills methods tools and techniques required for the safety of computer control systems *Dependable Computing - EDCC 2005* Mario Dal Cin,2005-04-13 This book constitutes the refereed proceedings of the 5th European Dependable Computing Conference EDCC 2005 held in Budapest Hungary in April 2005 The 21 revised full papers 5 revised practical experience reports and 4 prototype description papers presented together with the abstract of a keynote and 2 fast track papers were carefully reviewed and selected from 90 submissions The papers are organized in topical sections on distributed algorithms

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Nicolas Pallikarakis,Panagiotis D. Bamidis,2010-05-28 Over the past three decades the exploding number of new technologies and applications introduced in medical practice often powered by advances in biosignal processing and biomedical imaging created an amazing account of new possibilities for diagnosis and therapy but also raised major questions of appropriateness and safety The accelerated development in this field alongside with the promotion of electronic health care solutions is often on the basis of an uncontrolled diffusion and use of medical technology The emergence and use of medical devices is multiplied rapidly and today there exist more than one million different products available on the world market Despite the fact that the rising cost of health care partly resulting from the new emerging technological applications forms the most serious and urgent problem for many governments today another important concern is that of patient safety and user protection issues that should never be compromised and expelled from the Biomedical Engineering research practice agenda *Recent Advances In Information Science And Technology* Nikos E Mastorakis,1998-10-12 Recent Advances in Information Science and Technology brings you a balanced state of the art presentation of the latest concepts methods algorithms techniques procedures and applications of the fascinating field of Computer Science and Engineering Written by eminent leading international experts the contributors provide up to date aspects of topics discussed and present fresh original insights into their own experience with Information Science and Technology This rich anthology of papers which compose this volume contains the latest developments and reflects the experience of many eminent researchers working in different environments universities research centers and industry The book is composed of five parts Software Engineering in which new trends and recent scientific results in software engineering data structures algorithms knowledge based systems VLSI design computer languages and industrial computer applications are presented Signal Processing in which modern topics in signal processing identification recognition speech processing and detection are included Multi Dimensional m D Systems Theory and Applications which contains new research results in m D systems theory and impressive applications of multidimensional systems mainly in signal processing Communication Systems containing modern topics of communication as Digital systems of communication computer networks theory ATM networks optical networks hybrid fibber coaxial networks Internet etc Modern Numerical Techniques and Related Topics which covers some aspects of the modern computation science and technology **Proceedings of International Conference on Advanced Computing Applications** Jyotsna Kumar Mandal,Rajkumar Buyya,Debashis De,2021-11-23 This book gathers selected high quality research papers presented at the 2nd International Conference on Advanced Computing Applications ICACA 2021 held virtually during 27 28 March 2021 The book is divided into four sections These are communication and computing signal processing and multimedia computational intelligence and data analytics and decision computing The topics covered are advanced communication technologies IoT based systems and applications network security and reliability virtualization technologies compressed sensors and multimedia applications signal image and video processing machine learning pattern

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