

# LAB MANUAL

## Digital Logic Design Lab

B.Tech. III Semester CSE

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# Electronic Circuits Logic Design Lab Manual

**NAVAS, K. A.**



## **Electronic Circuits Logic Design Lab Manual:**

*ELECTRONICS LAB MANUAL (VOLUME 2)* NAVAS, K. A., 2018-10-01 This book is evolved from the experience of the author who taught all lab courses in his three decades of teaching in various universities in India The objective of this lab manual is to provide information to undergraduate students to practice experiments in electronics laboratories This book covers 118 experiments for linear analog integrated circuits lab communication engineering lab power electronics lab microwave lab and optical communication lab The experiments described in this book enable the students to learn Various analog integrated circuits and their functions Analog and digital communication techniques Power electronics circuits and their functions Microwave equipment and components Optical communication devices This book is intended for the B Tech students of Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics It is designed not only for engineering students but can also be used by BSc MSc Physics and Diploma students KEY FEATURES Contains aim components and equipment required theory circuit diagram pin outs of active devices design tables graphs alternate circuits and troubleshooting techniques for each experiment Includes viva voce and examination questions with their answers Provides exposure on various devices TARGET AUDIENCE B Tech Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics BSc MSc Physics Diploma Engineering      Digital Electronics and Microprocessors Lab Manual Mr. Rohit Manglik, 2024-04-06

EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels      Introduction to Logic Circuits & Logic Design with Verilog Brock J. LaMeres, 2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome

**The Hands-on XBEE Lab Manual** Jonathan A. Titus, 2012-05-18 Explains in practical terms the basic capabilities and potential uses of XBee modules and gives engineers the know how that they need to apply the technology to their networks

and embedded systems This book provides insight into the product data sheets It saves you time and helps you get straight to the information you need      Introduction to Logic Circuits & Logic Design with VHDL Brock J. LaMeres, 2019-03-19 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 13 or a single accelerated course that uses the early chapters as reference material      Lab Manual Latest Edition Dr. J. P. Goel, 2016-12-17 Lab E Manual Physics For XIIth Practicals A Every student will perform 10 experiments 5 from each section 8 activities 4 from each section during the academic year Two demonstration experiments must be performed by the teacher with participation of students The students will maintain a record of these demonstration experiments B Evaluation Scheme for Practical Examination One experiment from any one section 8 Marks Two activities one from each section 4 4 8 Marks Practical record experiments activities 6 Marks Record of demonstration experiments Viva based on these experiments 3 Marks Viva on experiments activities 5 Marks Total 30 Marks Section A Experiments 1 To determine resistance per cm of a given wire by plotting a graph of potential difference versus current 2 To find resistance of a given wire using metre bridge and hence determine the specific resistance of its material 3 To verify the laws of combination series parallel of resistances using a metre bridge 4 To compare the emf of two given primary cells using potentiometer 5 To determine the internal resistance of given primary cells using potentiometer 6 To determine resistance of a galvanometer by half deflection method and to find its figure of merit 7 To convert the given galvanometer of known resistance and figure of merit into an ammeter and voltmeter of desired range and to verify the same 8 To find the frequency of the a c mains with a sonometer Activities 1 To measure the resistance and impedance of an inductor with or without iron core 2 To measure resistance voltage AC DC current AC and check continuity of a given circuit using multimeter 3 To assemble a household circuit comprising three bulbs three on off switches a fuse and a power source 4 To assemble the components of a given electrical circuit 5 To study the variation in potential drop with length of a wire for a steady current 6 To draw the diagram of a given open circuit comprising at least a battery resistor

rheostat key ammeter and voltmeter Mark the components that are not connected in proper order and correct the circuit and also the circuit diagram

**Section B Experiments**

- 1 To find the value of  $v$  for different values of  $u$  in case of a concave mirror and to find the focal length
- 2 To find the focal length of a convex lens by plotting graphs between  $u$  and  $v$  or between  $1/u$  and  $1/v$
- 3 To find the focal length of a convex mirror using a convex lens
- 4 To find the focal length of a concave lens using a convex lens
- 5 To determine angle of minimum deviation for a given prism by plotting a graph between angle of incidence and angle of deviation
- 6 To determine refractive index of a glass slab using a travelling microscope
- 7 To find refractive index of a liquid by using i concave mirror ii convex lens and plane mirror
- 8 To draw the I V characteristic curve of a p n junction in forward bias and reverse bias
- 9 To draw the characteristic curve of a zener diode and to determine its reverse break down voltage
- 10 To study the characteristics of a common emitter npn or pnp transistor and to find out the values of current and voltage gains

**Activities**

- 1 To study effect of intensity of light by varying distance of the source on a L D R
- 2 To identify a diode a LED a transistor and IC a resistor and a capacitor from mixed collection of such items
- 3 Use of multimeter to i identify base of transistor ii distinguish between npn and pnp type transistors iii see the unidirectional flow of current in case of a diode and a LED iv check whether a given electronic component e g diode transistor or I C is in working order
- 4 To observe refraction and lateral deviation of a beam of light incident obliquely on a glass slab
- 5 To observe polarization of light using two Polaroids
- 6 To observe diffraction of light due to a thin slit
- 7 To study the nature and size of the image formed by i convex lens ii concave mirror on a screen by using a candle and a screen for different distances of the candle from the lens
- 8 To obtain a lens combination with the specified focal length by using two lenses from the given set of lenses

**Suggested Investigatory Projects**

- 1 To investigate whether the energy of a simple pendulum is conserved
- 2 To determine the radius of gyration about the centre of mass of a metre scale as a bar pendulum
- 3 To investigate changes in the velocity of a body under the action of a constant force and determine its acceleration
- 4 To compare effectiveness of different materials as insulators of heat
- 5 To determine the wavelengths of laser beam by diffraction
- 6 To study various factors on which the internal resistance emf of a cell depends
- 7 To construct a time switch and study dependence of its time constant on various factors
- 8 To study infrared radiations emitted by different sources using photo transistor
- 9 To compare effectiveness of different materials as absorbers of sound
- 10 To design an automatic traffic signal system using suitable combination of logic gates
- 11 To study luminosity of various electric lamps of different powers and make
- 12 To compare the Young's modulus of elasticity of different specimens of rubber and also draw their elastic hysteresis curve
- 13 To study collision of two balls in two dimensions
- 14 To study frequency response of i a resistor an inductor and a capacitor ii RL circuit iii RC circuit iv LCR series circuit

Lab Manual Troubleshooting and Design to Accompany Digital Systems Frank J. Ambrosio, 1997      Digital Logic Design MCQ (Multiple Choice Questions) Arshad Iqbal, 2019-06-11 The Digital Logic Design Multiple Choice Questions MCQ Quiz with Answers PDF Logic Design MCQ PDF Download Quiz Questions Chapter 1 12 Practice Tests with Answer Key

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*Computer Design Aids for VLSI Circuits* P. Antognetti, Donald O. Pederson, Hugo De Man, 2013-11-11 The Nato Advanced Study Institute on Computer Design Aids for VLSI Circuits was held from July 21 to August 1 1980 at Sogesta Urbino Italy Sixty three carefully chosen professionals were invited to participate in this institute together with 12 lecturers and 7 assistants The 63 participants were selected from a group of almost 140 applicants Each had the background to learn effectively the set of computer IC design aids which were presented Each also had individual expertise in at least one of the topics of the Institute The Institute was designed to provide hands on type of experience rather than consisting of solely

lecture and discussion Each morning detailed presentations were made concerning the critical algorithms that are used in the various types of computer IC design aids Each afternoon a lengthy period was used to provide the participants with direct access to the computer programs In addition to using the programs the individual could if his expertise was sufficient make modifications of and extensions to the programs or establish limitations of these present aids The interest in this hands on activity was very high and many participants worked with the programs every free hour The editors would like to thank the Direction of SOGESTA for the excellent facilities 1r R Riccioni of the SOGESTA Computer Center and Mr 11 Vanzi of the University of Genova for enabling all the programs to run smoothly on the set date P Antognetti D O Pederson Urbino

Summer 1980      Physics Lab Manual Neena Sinha, R Rangarajan, R P Manchanda, R K Gupta, Rajesh Kumar, Lab Manual

*A Practical Guide for Simulation and FPGA Implementation of Digital Design* Bekkay Hajji, Adel Mellit, Loubna Bouselham, 2022-03-21 This book introduces the FPGA technology used in the laboratory sessions and provides a step by step guide for designing and simulation of digital circuits It utilizes the VHDL language which is one of the most common language used to describe the design of digital systems The Quartus II Xilinx ISE 14.7 and ModelSim software are used to process the VHDL code and make simulations and then the Altera and Xilinx FPGA platforms are employed to implement the simulated digital designs The book is composed of four parts The first part of this book has two chapters and covers various aspects FPGA architectures ASIC vs FPGA comparison FPGA design flow and basic VHDL concepts necessary to describe the design of digital systems The second part of the book includes three chapters that deal with the design of digital circuits such as combinational logic circuits sequential logic circuits and finite state machines The third part of the book is reserved for laboratory projects carried out on the FPGA platform It is a largely hands on lab class for design digital circuits and implementing their designs on the Altera FPGA platform Finally the fourth part of this work is devoted to recent applications carried out on FPGAs in particular advanced techniques in renewable energy systems The book is primarily intended for students scholars and industrial practitioners interested in the design of modern digital systems      *Digital Design from Zero*

*to One* Jerry D. Daniels, 1996-04-19 Takes a fresh look at basic digital design From definition to example to graphic illustration to simulation result the book progresses through the main themes of digital design Technically up to date this book covers all the latest topics Field programmable gate arrays PALs and ROMs The latest memory chips for SRAM and DRAM are shown Software for creating the excitation equations of FSM are covered as well as LogicWorks and Beige Bag PC and more      Lab Manual-Physics-TB-12\_E-R Dr R K Gupta, Lab Manual Physics TB 12\_E R      *Industrial Electronics: A*

*Text-lab Manual* Paul B. Zbar, 1972      Computers and Data Processing Systems, 1962      U.S. Government Research Reports, 1962      **Scientific and Technical Aerospace Reports**, 1994      An Introductory Guide to EC Competition Law and Practice Valentine Korah, 1994      **Catalog of Copyright Entries. Third Series** Library of Congress. Copyright Office, 1974      Subject Guide to Books in Print, 1993



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