

1) (a) An ALP to transfer a given block of data from source memory block to destination memory block with out overlap

```
data segment
var1 dw 12h,34h,45h,67h,56h
cnt dw 5
res dw 2
data ends
code segment
assume cs:code,ds:data
start:  mov ax,data
        mov ds,ax
        mov ax,cnt
        mov si,0000h
next:   mov ax,var1[si]
        mov res[si],ax
        inc si
        inc si
        loop next
        mov ah,4ch
        int 21h
code ends
end start
```

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Intel Corporation



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