

Micros 3700 QUICK REFERENCE GUIDE – Server/Cashier Functions

How To Check In:

- At a workstation, click on CHECK IN & OUT SCREEN.



- Enter in your ID number on the number pad and hit



- If you have multiple job functions, choose one from the list and hit OK.



- You are now checked into the system.

How To Sign In:

- At a workstation, enter your ID number on the number pad



- Depending on your job assignment, a specific sign-in screen will appear.
- Begin your check from the point.

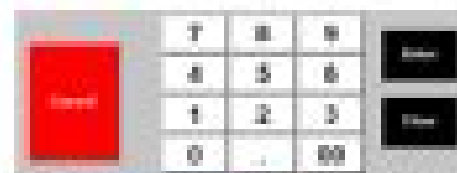
How To Begin A Check:

If you are a Server:

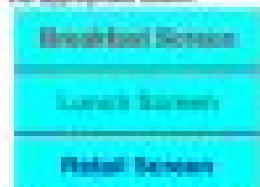
- Your Sign-in screen is the Point Plan. Choose a table to



- Begin your check.
- Enter in the number of guests on the number pad.



- Choose the menu you would like to order from by clicking on the appropriate button.



- Choose your menu category, e.g. Lunch Apps.



- Choose your menu item from the list that appears. All items are shown alphabetically, and can be viewed by scrolling up or down and the screen using the Page Up or Page Down



- To print your Order to the Manifest, click on the "Send Order" button.



How To Pick Up an Existing Check:

By table:

- Sign in to the workstation. Choose the table from the list that appears with the existing check by touching it.
- Choose the menu you wish to order from or go to the pay screen.
- If there are multiple checks on that table, choose the correct table from the list on the screen, and then choose the menu you wish to order from.

By the PUI Table screen:

- Sign in.
- Click on the "Existing Check" button.
- Choose the correct check from the screen.
- Choose the menu you would like to go to or go directly to the pay screen.



Making Corrections:

To remove the last item ordered:

- Press the "Back" button.

To remove an item by Touch & Hold:

- Highlight the item to be removed by touching it.
- Press the "Touch & Hold" button.

To completely start over:

- Press the "Start Over" button.

To void items already sent to remote printers (Kitchen/Bar):

- Highlight the item to be voided by touching it.
- Press the "Void" button.
- Have a manager approve the item and provide a reason.

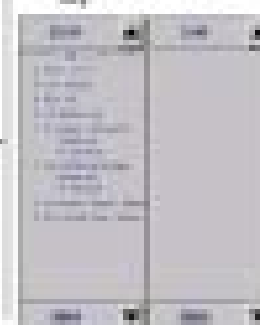
How to Split a Check:

- Pick up the menu.

- Press the "Split Check" button.



- Press the Check Functions screen, press the "Split Check" button.



- A new screen will appear with 2 columns.
- Move items to the other check by touch the item and then touching the "Move" button.
- To share an item, touch the item and then click the "Share" button.

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Jon Stokes



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Micro Saint Sharp User Manual v3_8 Beth Plott, Jake Pearson, Christopher Shaw, Marc Gacy, 2017-08-23 Micro Saint Sharp is a general purpose discrete event simulation software tool Micro Saint Sharp's intuitive graphical user interface and flow chart approach to modeling make it a tool that can be used by generalists as well as simulation experts Micro Saint Sharp has proven to be an invaluable asset in both small businesses and Fortune 500 companies and in many areas including the military human factors health care manufacturing and the service industry The user manual has been updated for software version 3.8 Some new features are the ability to add swim lanes to any network background data exchange capability with the UML SysML tool MagicDraw and a updated version of the built in OptQuest optimization The Micro-Trainer Manual Christopher E. Strangio, 1990 **ILO Manual for Labour Information Centres** International Labour Office, Laura Alpern, 1991 Micro, 1983 Procedural and declarative information in software manuals Nicole Ummelen, 2022-05-20 People who use software manuals want to get something done Procedural information directly supports this goal but the use of declarative information in manuals has often been under discussion Current research gives rise to the expectation that manual users tend to skip declarative information most of the time Also no effects of declarative information in software manuals have yet been found In this study information use and information effects in software manuals are investigated in three experiments thereby taking different user types different task types and different information arrangements into account A new technique was applied the click read method This technique enables the software user to use the manual and carry out software tasks at the same time while information selection and times are recorded automatically in logfiles For the first time quantitative data are presented about the amounts of procedural and declarative information that were selected and the times that were spent using these information types Although procedural information is selected more often and used longer declarative information appears to be a substantial part of the information selection Moreover the results show that using declarative information positively affects performance on future tasks performance on reasoning tasks and factual knowledge **Introduction to Computer Organization and Architecture** Mr. Rohit Manglik, 2024-02-12 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels *Introduction to Computer Organization & Architecture* Mr. Rohit Manglik, 2024-09-24 Covers the internal structure and functioning of computers including processors memory hierarchy instruction sets and input output mechanisms Builds a strong foundation for system level understanding **Computer Organization & Architecture** Er. Nagesh Jaitak, 2014-06-25 Computer organization Architecture is book related to hardware of Computer **A Practical Introduction to Hardware/Software Codesign** Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction

to the subject of embedded systems design with emphasis on integration of custom hardware components with software. The key problem addressed in the book is the following: how can an embedded systems designer strike a balance between flexibility and efficiency? The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem. The book covers four topics in hardware/software codesign: fundamentals, the design space of custom architectures, the hardware/software interface, and application examples. The book comes with an associated design environment that helps the reader to perform experiments in hardware/software codesign. Each chapter also includes exercises and further reading suggestions. Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera, which will make the material in this book applicable to a greater number of courses where these tools are already in use. More examples and exercises have been added throughout the book. If I were teaching a course on this subject, I would use this as a resource and text. If I were a student who wanted to learn codesign, I would look for a course that at least used a similar approach. If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective, I would read this book first before any other. When I first started learning about codesign as a practitioner, a book like this would have been the perfect introduction. Grant Martin, Tensilica

Modern X86 Assembly Language Programming Daniel Kusswurm, 2018-12-06 Gain the fundamentals of x86 64-bit assembly language programming and focus on the updated aspects of the x86 instruction set that are most relevant to application software development. This book covers topics including x86 64-bit programming and Advanced Vector Extensions (AVX) programming. The focus in this second edition is exclusively on 64-bit base programming architecture and AVX programming. Modern X86 Assembly Language Programming's structure and sample code are designed to help you quickly understand x86 assembly language programming and the computational capabilities of the x86 platform. After reading and using this book, you'll be able to code performance-enhancing functions and algorithms using x86 64-bit assembly language and the AVX, AVX2, and AVX-512 instruction set extensions. What You Will Learn: Discover details of the x86 64-bit platform, including its core architecture, data types, registers, memory addressing modes, and the basic instruction set. Use the x86 64-bit instruction set to create performance-enhancing functions that are callable from a high-level language (C). Employ x86 64-bit assembly language to efficiently manipulate common data types and programming constructs, including integers, text, strings, arrays, and structures. Use the AVX instruction set to perform scalar floating-point arithmetic. Exploit the AVX, AVX2, and AVX-512 instruction sets to significantly accelerate the performance of computationally intense algorithms in problem domains such as image processing, computer graphics, mathematics, and statistics. Apply various coding strategies and techniques to optimally exploit the x86 64-bit AVX, AVX2, and AVX-512 instruction sets for maximum possible performance. Who This Book Is For: Software developers who want to learn how to write code using x86 64-bit assembly language. It's also ideal for software developers who already have a basic understanding of x86 32-bit or 64-bit assembly language.

programming and are interested in learning how to exploit the SIMD capabilities of AVX AVX2 and AVX 512 **Official Gazette of the United States Patent and Trademark Office** United States. Patent and Trademark Office,2002

Fundamentals of Computer Architecture Mark Burrell,2017-03-14 Written for students taking their first course in computer systems architecture this is an introductory textbook that meets syllabus requirements in a simple manner without being a weighty tome The project is based around the simulation of a typical simple microprocessor so that students gain an understanding of the fundamental concepts of computer architecture on which they can build to understand the more advanced facilities and techniques employed by modern day microprocessors Each chapter includes a worked exercise end of chapter exercises and definitions of key words in the margins **Computer Organization and Design MIPS Edition** David A. Patterson,John L. Hennessy,2020-11-24 Computer Organization and Design The Hardware Software Interface Sixth Edition the leading award winning textbook from Patterson and Hennessy used by more than 40 000 students per year continues to present the most comprehensive and readable introduction to this core computer science topic Improvements to this new release include new sections in each chapter on Domain Specific Architectures DSA and updates on all real world examples that keep it fresh and relevant for a new generation of students Covers parallelism in depth with examples and content highlighting parallel hardware and software topics Includes new sections in each chapter on Domain Specific Architectures DSA Discusses and highlights the Eight Great Ideas of computer architecture including Performance via Parallelism Performance via Pipelining Performance via Prediction Design for Moore s Law Hierarchy of Memories Abstraction to Simplify Design Make the Common Case Fast and Dependability via Redundancy **Inside the Machine** Jon Stokes,2006-12-01 Computers perform countless tasks ranging from the business critical to the recreational but regardless of how differently they may look and behave they re all amazingly similar in basic function Once you understand how the microprocessor or central processing unit CPU works you ll have a firm grasp of the fundamental concepts at the heart of all modern computing Inside the Machine from the co founder of the highly respected Ars Technica website explains how microprocessors operate what they do and how they do it The book uses analogies full color diagrams and clear language to convey the ideas that form the basis of modern computing After discussing computers in the abstract the book examines specific microprocessors from Intel IBM and Motorola from the original models up through today s leading processors It contains the most comprehensive and up to date information available online or in print on Intel s latest processors the Pentium M Core and Core 2 Duo Inside the Machine also explains technology terms and concepts that readers often hear but may not fully understand such as pipelining L1 cache main memory superscalar processing and out of order execution Includes discussion of Parts of the computer and microprocessor Programming fundamentals arithmetic instructions memory accesses control flow instructions and data types Intermediate and advanced microprocessor concepts branch prediction and speculative execution Intermediate and advanced computing concepts instruction set architectures RISC and CISC the

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Embedded Signal Processing with the Micro Signal Architecture Woon-Seng Gan,Sen M. Kuo,2007-02-26 This is a real time digital signal processing textbook using the latest embedded Blackfin processor Analog Devices Inc ADI 20% of the text is dedicated to general real time signal processing principles The remaining text provides an overview of the Blackfin processor its programming applications and hands on exercises for users With all the practical examples given to expedite the learning development of Blackfin processors the textbook doubles as a ready to use user s guide The book is based on a step by step approach in which readers are first introduced to the DSP systems and concepts Although basic DSP concepts are introduced to allow easy referencing readers are recommended to complete a basic course on Signals and Systems before attempting to use this book This is also the first textbook that illustrates graphical programming for embedded processor using the latest LabVIEW Embedded Module for the ADI Blackfin Processors A solutions manual is available for adopters of the book from the Wiley editorial department **Computer Organization** James Gil de Lamadrid,2018-02-19

Computer Organization Basic Processor Structure is a class tested textbook based on the author s decades of teaching the topic to undergraduate and beginning graduate students The main questions the book tries to answer are how is a processor structured and how does the processor function in a general purpose computer The book begins with a discussion of the interaction between hardware and software and takes the reader through the process of getting a program to run It starts with creating the software compiling and assembling the software loading it into memory and running it It then briefly explains how executing instructions results in operations in digit circuitry The book next presents the mathematical basics required in the rest of the book particularly Boolean algebra and the binary number system The basics of digital circuitry are discussed next including the basics of combinatorial circuits and sequential circuits The bus communication architecture used in many computer systems is also explored along with a brief discussion on interfacing with peripheral devices The first part of the book finishes with an overview of the RTL level of circuitry along with a detailed discussion of machine language The second half of the book covers how to design a processor and a relatively simple register implicit machine is designed

ALU design and computer arithmetic are discussed next and the final two chapters discuss micro controlled processors and a few advanced topics *Computer Organisation & Assembly Language Programming* Mr. Rohit Manglik, 2024-06-24 Covers hardware architecture and low level programming using assembly language to understand CPU operations and memory management **InfoWorld** , 1989-02-06 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects

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