

SMA0/02: AS Paper 2 Part A Statistics Mark scheme

Question	Scheme	Marks	AOs
1(a)	$[Q_2 =] (5 +) \frac{12}{15} \times 5$	M1	1.1a
	$= 9$ or $(\text{use of } (n+1)) (5 +) \frac{12.5}{15} \times 5$	A1	1.1b
	or 9.166... awrt 9.17	(2)	
(b)	$[s_x =] \sqrt{\frac{5675}{30} - \left(\frac{355}{30}\right)^2} = \sqrt{49.14...}$	M1	1.1a
	= awrt 7.01	A1	1.1b
	Accept $\left[s_x = \sqrt{\frac{5675 - 30\left(\frac{355}{30}\right)^2}{29}} = 7.1294... \right]$	(2)	
(c)	$x = \frac{t + 15}{2}$ or $t = 2x + 15$	M1	3.1b
	Median = $2 \times 9 + 15 = 33$ (allow awrt 33.3 from "9.17" in (a))	A1ft	1.1b
	Sd = $2 \times 7.01 = 14.02...$ (awrt 14.0) [allow awrt 14.3 if s used]	A1ft	1.1b
		(3)	
(d)	The median time is "33" and "33" < 35 so 50% (30) should finish in 35 minutes.		
	ALT Probability of being < 35 mins is $\frac{18}{30} \quad \vee \quad \frac{18}{30} \times 60 = 36$ applicants to choose from.	M1	2.4
	It is likely that they will fill all 25 positions [providing those offered accept]	A1	2.2b
		(2)	
Notes: (9 marks)			
(a) M1: For a suitable fraction $\times 5$ (ignore end points) A1: For 9 or awrt 9.17 if using $n + 1$			
(b) M1: For a correct expression for s_x and S_x or s_x A1: For awrt $S_x = 7.01$ or $s_x =$ awrt 7.13			
(c) M1: For realising $x = \frac{t + 15}{2}$ and then rearranging to get a correct equation with t as the subject May be implied by a correct answer for the median of t . A1ft: ft their median A1ft: ft their S_x or s_x . NB using s gives awrt 14.3			
(d) M1: For a suitable comparison following through their value for the median of t . A1: A correct conclusion in context following through their value for the median of t .			

Mechanics 1 Summer 2013 Mark Scheme

AN Whitehead



Mechanics 1 Summer 2013 Mark Scheme:

Reviewing **Mechanics 1 Summer 2013 Mark Scheme**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is truly astonishing. Within the pages of "**Mechanics 1 Summer 2013 Mark Scheme**," an enthralling opus penned by a highly acclaimed wordsmith, readers embark on an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve to the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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web may 6 2012 a linear feedback shift register lfsr is a shift register whose input bit is the output of a linear function of two or more of its previous states taps an lfsr of length m consists of m stages numbered $0 \leq i \leq m-1$ each capable of storing one bit and a clock controlling data exchange

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web linear feedback shift registers lfsrs these are n bit counters exhibiting pseudo random behavior built from simple shift registers with a small number of xor gates used for random number generation counters error checking and correction advantages very little hardware high speed operation example 4 bit lfsr $q_4 \oplus q_3 \oplus q_2 \oplus q_1$

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web sep 28 2023 the applications of linear feedback shift registers and how to use our lfsr calculator what is an lfsr first what is a shift register it s a type of electronic logic circuit that stores and transmits data by moving one bit in a particular direction of a register at every step a basic type of computer memory

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web lfsr stands for linear feedback shift register and it is a design that is useful inside of fpgas lfsrs are simple to synthesize meaning that they take relatively few resources and can be run at very high clock rates inside of an fpga there are many applications that benefit from using an lfsr including counters test pattern generators

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web the present lfsr linear feedback shift register is modified by using the transition controller module the transition controller is utilized to raise the interrelation between successive test patterns

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web mar 30 2017 a linear feedback shift register lsfr is a shift register that takes a linear function of a previous state as an input most commonly this function is a boolean exclusive or xor the bits that affect the state in the other bits are known as taps lsfrs are used for digital counters cryptography and circuit testing advertisements

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web oct 28 2019 the algorithm couldn't be entirely linear because it would get stuck it would produce nothing but zeros forevermore once it encountered an input sequence of all zeros so technically a lfsr is an nearly always linear feedback shift register it's linear for $2^n - 2$ inputs and nonlinear for 2 special inputs a lfsr is more general

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input bit is driven by the xor of some bits of the overall shift register value

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