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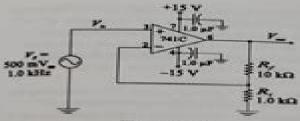


Figure 20-3

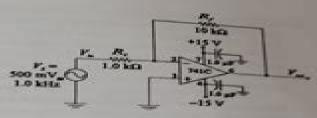


Figure 20-4

In this step you will test an inverting amplifier. All data are to be recorded in Table 20-2. The circuit is illustrated in Figure 20-4. The closed-loop gain is:

$$A_{cl(0)} = -\frac{R_f}{R_i}$$

- (a) Use the same resistors for R_j and R_i as in step 1. Record the measured value of resistance in Table 20-2.
- (b) Using the measured resistance, compute and record the closed-loop gain of the inverting amplifier.
- (c) Calculate V using the computed closed-loop gain.
- (d) Connect the circuit shown in Figure 20-4. Set the signal generator for a 500 mV is sine wave at 1 kHz. The generator should have no dc offset.
- (e) Measure and record the output voltage, V___
- (f) Measure and record the voltage at pin 2. This point is called a virtual ground because of the effect of negative feedback.
- (g) Place a 1.0 kf2 test resistor in series with the generator and R_c . Measure the input resistance of the circuit, R_{in} , based on the voltage drop across the test resistor. In this case, you should observe that the drop across the test resistor is about the same as the drop across R_c .

Table 20-2 *

R, Measured	R, Measured	V _m Measured	A _{cen} Computed	V _{cor} Computed Measured	V _{r-1} Measured (pm 2)	R _{in}
		500 mV _m	- 100 3			

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