



Figure 20-3



Figure 20-4

2. In this step you will test an inverting amplifier. All data are to be recorded in Table 20-2. The circuit is illustrated in Figure 20-4. The closed-loop gain is:

$$A_{cl(D)} = -\frac{R_f}{R_i}$$

- Use the same resistors for  $R_f$  and  $R_i$  as in step 1. Record the measured value of resistance in Table 20-2.
- Using the measured resistance, compute and record the closed-loop gain of the inverting amplifier.
- Calculate  $V_{out}$  using the computed closed-loop gain.
- Connect the circuit shown in Figure 20-4. Set the signal generator for a 500 mV<sub>pp</sub> sine wave at 1 kHz. The generator should have no dc offset.
- Measure and record the output voltage,  $V_{out}$ .
- Measure and record the voltage at pin 2. This point is called a *virtual ground* because of the effect of negative feedback.
- Place a 1.0 kΩ test resistor in series with the generator and  $R_i$ . Measure the input resistance of the circuit,  $R_{in}$ , based on the voltage drop across the test resistor. In this case, you should observe that the drop across the test resistor is about the same as the drop across  $R_i$ .

Table 20-2

$R_f$ Measured	$R_i$ Measured	$V_{in}$ Measured	$A_{cl(D)}$ Computed	$V_{out}$ Computed Measured (pin 6)	$V_{pin 2}$ Measured (pin 2)	$R_{in}$ Measured
		500 mV <sub>pp</sub>				

# Floyd Buchla Analog Fundamentals Experiments Answers

**Siddappa N.Byrareddy**



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