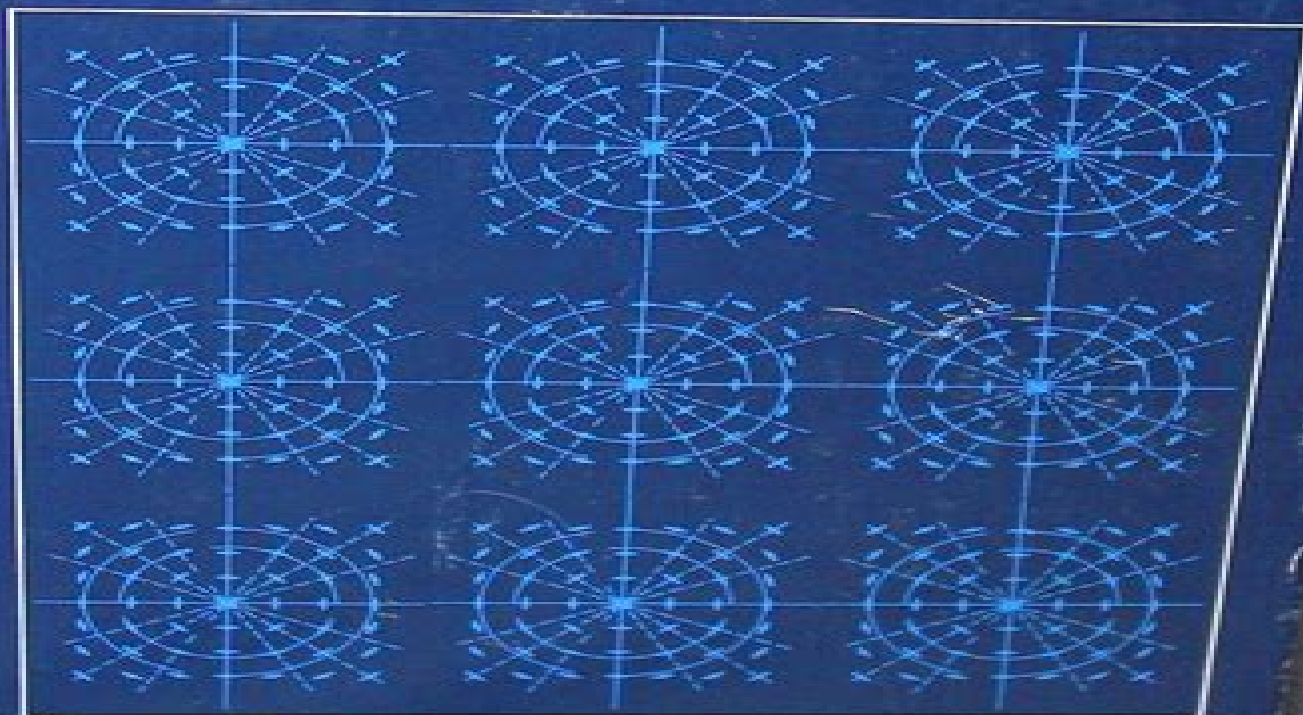


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insight that automatic and high precision phase measurement techniques will play a key role in all future industrial and scientific applications of optical metrology A couple of months later more than 50 specialists from East and West met in East Berlin the capital of the former GDR to spend 3 days with the discussion of new principles of fringe processing In the stimulating atmosphere the idea was born to repeat the workshop and to organize the meeting in an olympic schedule And thus meanwhile 20 years have been passed and we have today Fringe number six However such a workshop takes place in a dynamic environment Therefore the main topics of the previous events were always adapted to the most interesting subjects of the new period In 1993 the workshop took place in Bremen and was dedicated to new principles of optical shape measurement setup calibration phase unwrapping and nondestructive testing while in 1997 new approaches in multi sensor metrology active measurement strategies and hybrid processing technologies played a central role 2001 the first meeting in the 21st century was focused to optical methods for micrometrology hybrid measurement technologies and new sensor solutions for industrial inspection

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**Embedded Systems** Keh-La Lin, Armin Kemna, Bedrich J. Hosticka, 2006-01-14 One of the main trends of microelectronics is toward design for integrated systems i.e. system on a chip SoC or system on silicon SoS Due to this development design techniques for mixed signal circuits become more important than before Among other devices analog to digital and digital to analog converters are the two bridges between the analog and the digital worlds Besides low power design technique is one of the main issues for embedded systems especially for hand held applications Modular Low Power High Speed CMOS Analog to Digital Converter for Embedded Systems aims at design techniques for low power high speed analog to digital converter processed by the standard CMOS technology Additionally this book covers physical integration issues of A/D converter integrated in SoC i.e. substrate crosstalk and reference voltage network design

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