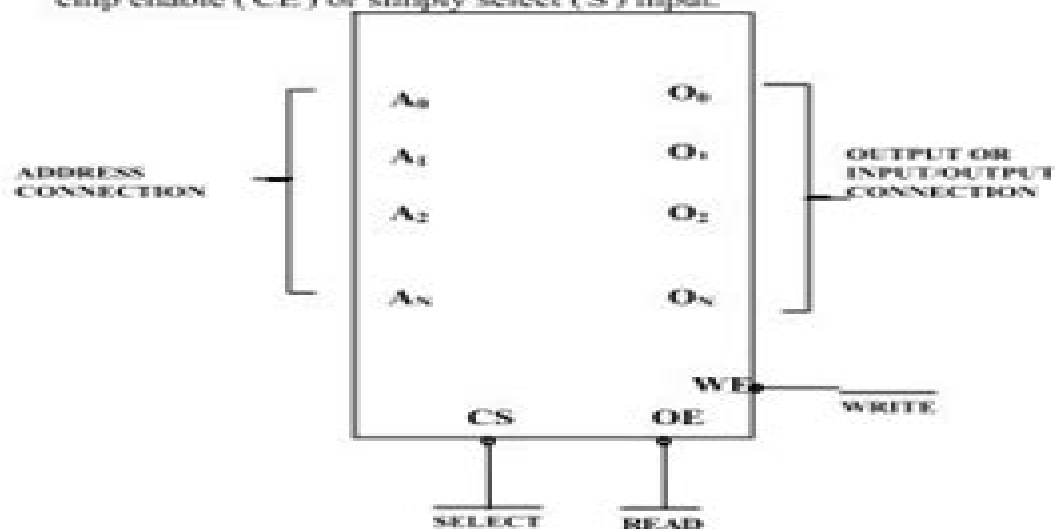


## Module 3

## Learning unit 8: Interface

- We have four common types of memory:
- Read only memory (ROM)
- Flash memory (EEPROM)
- Static Random access memory (SARAM)
- Dynamic Random access memory (DRAM).
- Pin connections common to all memory devices are: The address input, data output or input/outputs, selection input and control input used to select a read or write operation.
- **Address connections:** All memory devices have address inputs that select a memory location within the memory device. Address inputs are labeled from  $A_0$  to  $A_n$ .
- **Data connections:** All memory devices have a set of data outputs or input/outputs. Today many of them have bi-directional common I/O pins.
- **Selection connections:** Each memory device has an input, that selects or enables the memory device. This kind of input is most often called a chip select ( $\overline{CS}$ ), chip enable ( $\overline{CE}$ ) or simply select ( $\overline{S}$ ) input.



#### MEMORY COMPONENT ILLUSTRATING THE ADDRESS, DATA AND CONTROL CONNECTIONS

- RAM memory generally has at least one  $\overline{CS}$  or  $\overline{S}$  input and ROM at least one  $\overline{CE}$ .
- If the  $\overline{CE}$ ,  $\overline{CS}$ ,  $\overline{S}$  input is active the memory device perform the read or write.

# Lecture Notes On 8086 Microprocessor By Nptel

**E Durkheim**



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