CPU Support List for PVM7 PRO

CPU Support list by Model
Model: PVM7 PRO
Chipset: VIA® P4M800Pro / 8237R

Processor	Processor Info. (Cache, System Bus*, Pkg)	Status
Pentium Extreme Edition	3.4G (2M,800,Socket 775)	YES
Pentium Extreme Edition	3.2G (2M,800,Socket 775)	YES
Pentium D	3.2G (2x2M,800,Socket 775)	YES
Pentium D	2.66G (2x1M,533,Socket 775)	YES
Pentium D	2.8G (2x2M,800,Socket 775)	YES
Pentium D 950	3.4G (2x2M,800,Socket 775)	YES
Pentium D 930	3.0G (2x2M,800,Socket 775)	YES
Pentium D 920	2.8G (2x2MB,800,Socket 775)	YES
P4 672	3.8G (2M,800,Socket 775)	YES
P4 670	3.8G (2M,800,Socket 775)	YES
P4 662	3.6G (2M,800,Socket 775)	YES
P4 661	3.6G (2M,800,Socket 775)	YES
P4 660	3.6G (2M,800,Socket 775)	YES
P4 651	3.4G (2M,800,Socket 775)	YES
P4 650	3.4G (2M,800,Socket 775)	YES
P4 641	3.2G (2M,800,Socket 775)	YES
P4 640	3.2G (2M,800,Socket 775)	YES
P4 631(D0)	3.0G (2M,800,Socket 775)	YES
P4 630	3.0G (2M,800,Socket 775)	YES
P4 571	3.8G (1M,800,Socket 775)	YES
P4 570J	3.8G (1M,800,Socket 775)	YES
P4 561	3.6G (1M,800,Socket 775)	YES
P4 560J	3.6G (1M,800,Socket 775)	YES
P4 560	3.6G (1M,800,Socket 775)	YES
P4 551	3.4G (1M,800,Socket 775)	YES
P4 550J	3.4G (1M,800,Socket 775)	YES
P4 550	3.4G (1M,800,Socket 775)	YES
P4 541	3.2G (1M,800,Socket 775)	YES
P4 540J	3.2G (1M,800,Socket 775)	YES
P4 540	3.2G (1M,800,Socket 775)	YES
P4 531	3.0G (1M,800,Socket 775)	YES

Mercury Pvm7 Motherboard Manual

Mercury Marine

Mercury Pvm7 Motherboard Manual:

Mercury Marine Service Manual, Mercury Outboards 1965 and Prior Kiekhaefer Mercury. Fon du Lac, Wis,1958

VAX MERCURY User's Manual Jed E. Marquart,WRIGHT RESEARCH AND DEVELOPMENT CENTER WRIGHT-PATTERSON AFB OH.,1990 This report describes the use and operation of the MERCURY flow solver code on the Digital Equipment VAX series of computers MERCURY is a multiple grid block flow solver which solves the Euler set of equations inviscid flow The MERCURY code was developed and written by William Z Strang of WRDC FIMM for use on the CRAY Supercomputer reference Appendix D As such it was optimized to take full advantage of CRAY specific I O routines which greatly reduce the I O time required Since a large percentage of execution time is spent in the I O process when using a multiblock code such as this the optimization of the I O process is essential to achieving acceptable turnaround times for the solution In addition to the CRAY specific I O routines MERCURY was developed using one dimensional arrays to reduce memory overhead and improve vectorization While the optimization of the I O routines for operation on the CRAY makes MERCURY an extremely fast code it also makes the code much less portable Other machines do not permit the type of I O which makes the CRAY so desirable Thus when it is desired to convert the code to run on another host machine the I O routines must be changed to work on the new host while still maintaining the correct data structure and logical order kr

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    - Chapter 2: Essential Elements of Mercury Pvm7 Motherboard Manual
    - Chapter 3: Mercury Pvm7 Motherboard Manual in Everyday Life
    - Chapter 4: Mercury Pvm7 Motherboard Manual in Specific Contexts
    - Chapter 5: Conclusion
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