

Unit 4: Solid state Physics

MCQ Question Bank

Q. No	Question	Answer
1	When a P-type and N-type semiconductors are joined together to form a PN junction, what happens to the Fermi energy levels (a) Fermi energy level decreases for N-type while increases for P-type until equilibrium is obtained (b) Fermi energy level increases for N-type while decreases for P-type until equilibrium is obtained (c) Fermi energy level remains constant but the distribution of holes and electrons changes. (d) Fermi energy level remains unchanged and equilibrium is obtained	A
2	What happens to the Fermi energy level if a forward bias is applied to a PN junction diode? (a) Fermi energy level decreases for N-type while increases for P-type (b) Fermi energy level increases for N-type while decreases for P-type (c) Fermi energy levels remains in equilibrium in both regions to allow current to flow (d) Fermi energy levels increases for both the regions	B
3	What happens to the Fermi energy level if a reverse bias is applied to a PN junction diode? (a) Fermi energy level decreases for N-type while increases for P-type (b) Fermi energy level increases for N-type while decreases for P-type (c) Fermi energy levels remains in equilibrium in both regions to allow current to flow (d) Fermi energy levels increases for both the regions	A
4	When a P-type and N-type semiconductors are joined to form a PN junction (a) The electrons are transferred from P-region to N-region and Fermi energy level of both regions decreases (b) The electrons are transferred from P-region to N-region and Fermi energy level of both regions increases (c) The electrons are transferred from N-region to P-region and Fermi energy level both regions attains equilibrium (d) The electrons are transferred from N-region to P-region and Fermi energy level of both regions increases	C
5	In equilibrium state of a PN junction diode (a) Fermi energy level of P-region is higher than that of N-region (b) Fermi energy level of N-region is lower than that of P-region (c) Fermi energy levels of both the regions are at minimum value (d) Fermi energy levels of both the regions attend thermal equilibrium	D
6	In an equilibrium state of an NPN transistor, what is true for Fermi energy levels (a) (Fermi energy emitter) > (Fermi energy base) > (Fermi energy collector) (b) (Fermi energy emitter) < (Fermi energy base) > (Fermi energy collector) (c) (Fermi energy emitter) = (Fermi energy base) = (Fermi energy collector) (d) (Fermi energy emitter) > (Fermi energy base) < (Fermi energy collector)	C
7	For a biased NPN transistor, what is true for Fermi energy levels (a) (Fermi energy emitter) > (Fermi energy base) > (Fermi energy collector) (b) (Fermi energy emitter) < (Fermi energy base) > (Fermi energy collector) (c) (Fermi energy emitter) = (Fermi energy base) = (Fermi energy collector) (d) (Fermi energy emitter) > (Fermi energy base) < (Fermi energy collector)	A
8	Hall effect is true for (a)Metals only (b)Semiconductors only (c)Both metals and semiconductors (d)For N-type semiconductors only	C

Mcq In Solid State Physics

Arshad Iqbal



Mcq In Solid State Physics:

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Table of Contents Mcq In Solid State Physics

1. Understanding the eBook Mcq In Solid State Physics
 - The Rise of Digital Reading Mcq In Solid State Physics
 - Advantages of eBooks Over Traditional Books
2. Identifying Mcq In Solid State Physics
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Mcq In Solid State Physics
 - User-Friendly Interface
4. Exploring eBook Recommendations from Mcq In Solid State Physics
 - Personalized Recommendations
 - Mcq In Solid State Physics User Reviews and Ratings
 - Mcq In Solid State Physics and Bestseller Lists
5. Accessing Mcq In Solid State Physics Free and Paid eBooks

- Mcq In Solid State Physics Public Domain eBooks
 - Mcq In Solid State Physics eBook Subscription Services
 - Mcq In Solid State Physics Budget-Friendly Options
6. Navigating Mcq In Solid State Physics eBook Formats
 - ePub, PDF, MOBI, and More
 - Mcq In Solid State Physics Compatibility with Devices
 - Mcq In Solid State Physics Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Mcq In Solid State Physics
 - Highlighting and Note-Taking Mcq In Solid State Physics
 - Interactive Elements Mcq In Solid State Physics
 8. Staying Engaged with Mcq In Solid State Physics
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Mcq In Solid State Physics
 9. Balancing eBooks and Physical Books Mcq In Solid State Physics
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Mcq In Solid State Physics
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Mcq In Solid State Physics
 - Setting Reading Goals Mcq In Solid State Physics
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Mcq In Solid State Physics
 - Fact-Checking eBook Content of Mcq In Solid State Physics
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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