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Question Paper Code : 40394

M.E./M.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2018.

Elective

M.E. Communication Systems

CU 5092 — REAL TIME EMBEDDED SYSTEMS

(Common to M.E. Communication and Networking/M.E. Medical Electronics)

(Regulations 2017)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. What are the advantages and disadvantages of load-store multiple instructions?
2. Mention the importance of instruction scheduling and conditional execution of ARM instructions.
3. What are the debugging challenges?
4. Draw the timing diagram of Bus read and write operation.
5. What are the three conditions that must be examined by the re-entrant function?
6. How is a Real time operating system uniquely different than a general purpose OS?
7. Draw the CAN data frame packet format.
8. Differentiate single hop network from multi hop network.
9. Why most designers use FOSS tools in embedded system development?
10. What are the methods for testing a software modem?

Embedded Systems Anna University Question Paper

**Radek Silhavy, Petr Silhavy, Zdenka
Prokopova**



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web although our aim is to give an understanding of latchup as it occurs in cmos switches similar principles apply to many other cmos devices latchup may be defined as the creation of a low impedance path between power supply rails as a result of triggering a parasitic device

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web may 10 2020 technically latch up is the phenomena of activating the parasitic bjts in a cmos circuit which forms a low impedance path between the power and ground terminals this low impedance path draws a large current and heats up the ic integrated chip which cause permanent damage of ic