

2 SK 2633(LS)	MOS-N-FET-ø	V-MOS, 800V, 2.5A, 25W, <4.8Ω(1.3A)	17c
2 SK 2634	MOS-N-FET-ø	V-MOS, 800V, 3A, 30W, <3.6Ω(1.8A)	17c
2 SK 2635	MOS-N-FET-ø	V-MOS, 800V, 3.5A, 70W, <2.5Ω(1.5A)	17c
2 SK 2636	MOS-N-FET-ø	V-MOS, 800V, 3.5A, 40W, <2.5Ω(1.5A)	17c
2 SK 2637	MOS-N-FET-ø	V-MOS, 800V, 7A, 50W, <1.2Ω(4.5A)	17c
2 SK 2638-01MR	MOS-N-FET-ø	SMD, V-MOS, 20V, 8A, 2W, <0.05Ω(5A)	17c
2 SK 2639-01MR	MOS-N-FET-ø	V-MOS, 40V, 10A, 50W, <1.2Ω(4.5A)	17c

Ohms Value



Type	Device	Short Description	Fig.
2 SK 2639-01	MOS-N-FET-ø	=2SK2638 100W	18c5
2 SK 2640-01MR	MOS-N-FET-ø	V-MOS, 500V, 10A, 50W, <0.3Ω(5A)	17c
2 SK 2641-01	MOS-N-FET-ø	=2SK2640 100W	18c5
2 SK 2643-01	MOS-N-FET-ø	V-MOS, 500V, 15A, 125W, <2.5Ω(7.5A)	18c5
2 SK 2645-01MR	MOS-N-FET-ø	V-MOS, 600V, 9A, 50W, <1.2Ω(4.5A)	17c
2 SK 2646-01(MR)	MOS-N-FET-ø	V-MOS, 800V, 4A, 80W, <4Ω(2A)	17c5
2 SK 2647-01MR	MOS-N-FET-ø	=2SK2646 40W	17c
2 SK 2648-01	MOS-N-FET-ø	V-MOS, 800V, 9A, 150W, <1.5Ω(4.5A)	18c5
2 SK 2649-01R	MOS-N-FET-ø	=2SK2648 150, 100W	18c
2 SK 2651-01MR	MOS-N-FET-ø	V-MOS, 900V, 6A, 50W, <2.5Ω(3A)	17c
2 SK 2652-01	MOS-N-FET-ø	=2SK2651 125W	18c5
2 SK 2653-01R	MOS-N-FET-ø	=2SK2651 80W	18c
2 SK 2654-01	MOS-N-FET-ø	V-MOS, 900V, 8A, 150W, <2Ω(4A)	18c5
2 SK 2655-01R	MOS-N-FET-ø	=2SK2654 150, 100W	18c5

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MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike *Power MOSFET Products* GE Corporation. Solid State Division, 1987

Guide to State-of-the-Art Electron Devices Joachim N. Burghartz, 2013-03-19 Winner 2013 PROSE Award Engineering and Technology Concise high quality and comparative overview of state of the art electron device development manufacturing technologies and applications Guide to State of the Art Electron Devices marks the 60th anniversary of the IRE electron devices committee and the 35th anniversary of the IEEE Electron Devices Society as such it defines the state of the art of electron devices as well as future directions across the entire field Spans full range of electron device types such as photovoltaic devices semiconductor manufacturing and VLSI technology and circuits covered by IEEE Electron and Devices Society Contributed by internationally respected members of the electron devices community A timely desk reference with fully integrated colour and a unique lay out with sidebars to highlight the key terms Discusses the historical developments and speculates on future trends to give a more rounded picture of the topics covered A valuable resource R engineers in the semiconductor industry applied scientists circuit designers Masters students in power electronics and members of the IEEE Electron Device Society

Fundamentals Of Solid-state Electronics: Study Guide Chih Tang Sah, 1994-02-08 This companion to Fundamentals of Solid State Electronics provides a helpful summary of the

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