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Question Paper Code : X10405

**B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2020 AND
APRIL/MAY 2021**

**Sixth/Seventh Semester
Electronics and Instrumentation Engineering
EE 8691 – EMBEDDED SYSTEMS
(Common to Electrical and Electronics Engineering)
(Regulations 2017)**

Time : Three Hours

Maximum : 100 Marks

Answer ALL questions

PART – A

(10×2=20 Marks)

1. Define different categories and requirements of embedded systems.
2. Name some of the hardware parts of embedded systems.
3. Differentiate synchronous communication and Iso-synchronous communication.
4. Draw the data frame format of CAN.
5. Classify the difference between state machine model and sequential model.
6. Draw the classic embedded product development life cycle model.
7. Distinguish non preemptive and preemptive scheduling in RTOS.
8. Justify the advantages of mail boxes in RTOS.
9. Mention the importance of input interface and output interface in Washing machine.
10. State the role of interfacing I/O circuit in smart card.

PART – B

(5×13=65 Marks)

11. a) With neat diagram, explain the working of Direct Memory Access (DMA) and mention the memory management methods.

(OR)

- b) i) Explain how suitable processor and memory devices are selected for an embedded system design. **(6)**
- ii) Explain the function of timing and counting devices in embedded systems. **(7)**

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**Sven Behnke,Manuela M.
Veloso,Arnoud Visser,Rong Xiong**

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