

M.Tech. Degree Examination, Dec.2013/Jan.2014
Advanced Microcontrollers

Time: 3 hrs.

Max. Marks:100

Note: Answer any FIVE full questions.

1.
 - a. What is the need for low power embedded system? Explain briefly. (04 Marks)
 - b. Briefly explain the different power saving techniques to achieve low power. (06 Marks)
 - c. Explain with a neat block diagram, the architecture of MSP430 microcontroller. (10 Marks)
2.
 - a. What is meant by emulated instruction as referred to MSP430. Explain at least three emulated instruction with examples and its emulation. (05 Marks)
 - b. Write a note on the three internal clocks in MSP430. (03 Marks)
 - c. Explain the different addressing modes used in MSP430 microcontroller with suitable examples. (12 Marks)
3.
 - a. With reference to MSP430 microcontroller explain the difference between POR and PUC. (05 Marks)
 - b. What is resolution, precision and accuracy with respect to ADC? With a neat block diagram, explain the operation of successive approximation ADC of MSP430. (10 Marks)
 - c. For the circuit shown in Fig.Q.3(c), write a C program for MSP430 to turn ON an LED if the voltage exceeds $1/2 V_{CC}$. The clock used is internal. (05 Marks)



Fig.Q.3(c)

4.
 - a. What are the features of Real Time Clock (RTC) of MSP430? With a neat block diagram of RTC explain the operation in counter mode and calendar mode for MSP430. (10 Marks)
 - b. With the neat block diagram, explain the non-inverting op amp configuration for MSP430 microcontroller. (10 Marks)
5.
 - a. Explain the architecture of CORTEX-M3 processor with a block diagram. (10 Marks)
 - b. Write a short note on the following with respect to ARM CORTEX-M3:
 i) Link register; ii) Program status register. (06 Marks)
 - c. Explain the memory map of CORTEX-M3 processor. (04 Marks)
6.
 - a. Write a note on different types of faults with respect to CORTEX M3. Describe the different methods of handling these faults. (10 Marks)
 - b. Describe the context switching with pendable service call. (10 Marks)

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Robert R. Redfield



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Table of Contents Embedded Systems Vtu Question Papers

1. Understanding the eBook Embedded Systems Vtu Question Papers
 - The Rise of Digital Reading Embedded Systems Vtu Question Papers
 - Advantages of eBooks Over Traditional Books
2. Identifying Embedded Systems Vtu Question Papers
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Embedded Systems Vtu Question Papers
 - User-Friendly Interface
4. Exploring eBook Recommendations from Embedded Systems Vtu Question Papers
 - Personalized Recommendations
 - Embedded Systems Vtu Question Papers User Reviews and Ratings
 - Embedded Systems Vtu Question Papers and Bestseller Lists
5. Accessing Embedded Systems Vtu Question Papers Free and Paid eBooks
 - Embedded Systems Vtu Question Papers Public Domain eBooks
 - Embedded Systems Vtu Question Papers eBook Subscription Services

- Embedded Systems Vtu Question Papers Budget-Friendly Options
- 6. Navigating Embedded Systems Vtu Question Papers eBook Formats
 - ePub, PDF, MOBI, and More
 - Embedded Systems Vtu Question Papers Compatibility with Devices
 - Embedded Systems Vtu Question Papers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Embedded Systems Vtu Question Papers
 - Highlighting and Note-Taking Embedded Systems Vtu Question Papers
 - Interactive Elements Embedded Systems Vtu Question Papers
- 8. Staying Engaged with Embedded Systems Vtu Question Papers
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Embedded Systems Vtu Question Papers
- 9. Balancing eBooks and Physical Books Embedded Systems Vtu Question Papers
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Embedded Systems Vtu Question Papers
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Embedded Systems Vtu Question Papers
 - Setting Reading Goals Embedded Systems Vtu Question Papers
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Embedded Systems Vtu Question Papers
 - Fact-Checking eBook Content of Embedded Systems Vtu Question Papers
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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