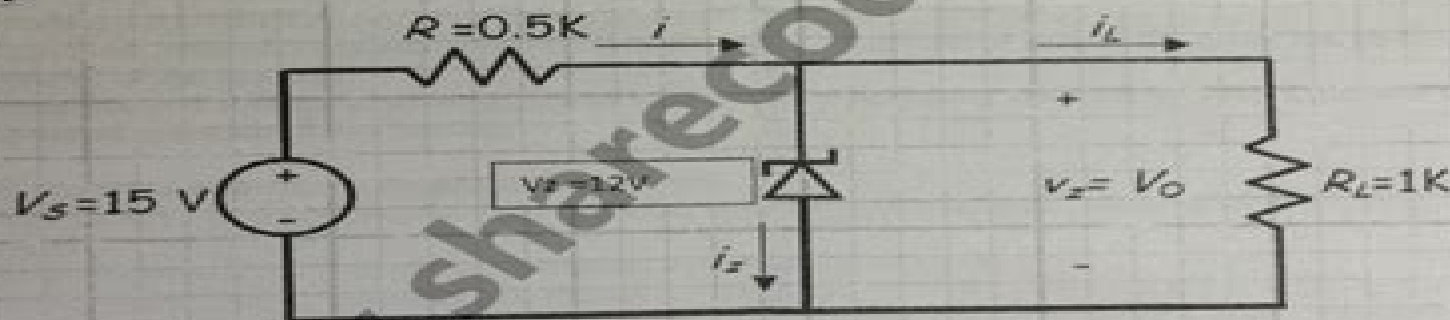


1. This paper contains 8 questions.
2. Question number 1 is compulsory.
3. Attempt any 5 questions from Q2 to Q8.
4. Attempt all the parts of the question chosen.
5. The marks assigned to each question are shown at the end of the question in square brackets.
6. Do not write anything on the question paper except your registration number at the designated space.
7. Answer all the questions in serial order.

- Q1(a) Explain Fermi Level of P-type Semiconductor [2 Marks]
- (b) Write the limitations of h parameter? [2 Marks]
- (c) Explain the V-I characteristic of diode? [2 Marks]
- (d) Explain the different Transistor current components with the help of diagram? [2 Marks]
- (e) List the three important characteristics of an Emitter follower. [2 Marks]
- (f) Differentiate between the depletion and enhancement mode MOSFETS? [2 Marks]
- (g) How does the FET behave a) for small values of $|V_{GS}|$ b) for large $|V_{GS}|$ [2 Marks]
- (h) Find output voltage V_o



- (i) Write down drawbacks of push pull amplifier? [2 Marks]
- (ii) A power transistor working in class A operation has zero signal power dissipation of 10 watt. If the a.c. output power is 3 watt, find (i) collector efficiency, (ii) power rating of transistor? [2 Marks]
- (k) Derive the expression for overall Efficiency of Class A amplifier? [2 Marks]
- (l) List advantages of negative feedback amplifier? [3 Marks]
- Q2 Draw the hybrid model of an common base transistor configuration. In terms of h-parameters, source resistance and load resistance derive input impedance, current gain, voltage gain and output admittance of CB configuration. [15 Marks]
- Q3 Derive the expression for Miller theorem by using Miller's equivalent circuits and explain any two Transistor applications? [15 Marks]
- Q4 With the aid of neat diagram describe Hall effect for a p-type semiconductor. And also derive the expressions for Hall's voltage and Hall's coefficient. Mention its applications. [15 Marks]

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