

Unit 4: Solid state Physics

MCQ Question Bank

Q. No	Question	Answer
1	When a P-type and N-type semiconductors are joined together to form a PN junction, what happens to the Fermi energy levels (a) Fermi energy level decreases for N-type while increases for P-type until equilibrium is obtained (b) Fermi energy level increases for N-type while decreases for P-type until equilibrium is obtained (c) Fermi energy level remains constant but the distribution of holes and electrons changes. (d) Fermi energy level remains unchanged and equilibrium is obtained	A
2	What happens to the Fermi energy level if a forward bias is applied to a PN junction diode? (a) Fermi energy level decreases for N-type while increases for P-type (b) Fermi energy level increases for N-type while decreases for P-type (c) Fermi energy levels remains in equilibrium in both regions to allow current to flow (d) Fermi energy levels increases for both the regions	B
3	What happens to the Fermi energy level if a reverse bias is applied to a PN junction diode? (a) Fermi energy level decreases for N-type while increases for P-type (b) Fermi energy level increases for N-type while decreases for P-type (c) Fermi energy levels remains in equilibrium in both regions to allow current to flow (d) Fermi energy levels increases for both the regions	A
4	When a P-type and N-type semiconductors are joined to form a PN junction (a) The electrons are transferred from P-region to N-region and Fermi energy level of both regions decreases (b) The electrons are transferred from P-region to N-region and Fermi energy level of both regions increases (c) The electrons are transferred from N-region to P-region and Fermi energy level both regions attains equilibrium (d) The electrons are transferred from N-region to P-region and Fermi energy level of both regions increases	C
5	In equilibrium state of a PN junction diode (a) Fermi energy level of P-region is higher than that of N-region (b) Fermi energy level of N-region is lower than that of P-region (c) Fermi energy levels of both the regions are at minimum value (d) Fermi energy levels of both the regions attend thermal equilibrium	D
6	In an equilibrium state of an NPN transistor, what is true for Fermi energy levels (a) (Fermi energy emitter) > (Fermi energy base) > (Fermi energy collector) (b) (Fermi energy emitter) < (Fermi energy base) > (Fermi energy collector) (c) (Fermi energy emitter) = (Fermi energy base) = (Fermi energy collector) (d) (Fermi energy emitter) > (Fermi energy base) < (Fermi energy collector)	C
7	For a biased NPN transistor, what is true for Fermi energy levels (a) (Fermi energy emitter) > (Fermi energy base) > (Fermi energy collector) (b) (Fermi energy emitter) < (Fermi energy base) > (Fermi energy collector) (c) (Fermi energy emitter) = (Fermi energy base) = (Fermi energy collector) (d) (Fermi energy emitter) > (Fermi energy base) < (Fermi energy collector)	A
8	Hall effect is true for (a)Metals only (b)Semiconductors only (c)Both metals and semiconductors (d)For N-type semiconductors only	C

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M Carnoy



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