AGJ 1st half (r)con-code 878 Con. 6634-13. 31/05/13

GS-7302

0.5

10

10

(3 Hours)

ours) [Total Marks : 100

N.B.	:(1)	Question No.1 is Compulsory.	
	(2)	Solve any four questions from the remaining.	
	(3)	Assume suitabale data wherever necessary and justify the same.	
1.			20
	(a)	Draw the V-I characteristics of SCR with and without gate control and mark holding current and latching current? Explain the difference between holding current and latching current	
	(b)	Explain why cyclo converters are suitable only for low frequency application?	
	(c)	Explain the meaning of following instructions a) LDA address b)MVI M,12H	
	(d)	Realize EX-OR gate by NAND gate	
	(c)	Why RC triggering is preferred over Resistance triggering process?	
2.	(a)	Explain single phase full wave fully controlled bridge rectifier with the help of circuit diagram and waveforms.	10
	(p)	Explain the UJT relaxation circuit to trigger SCR along with appropriate waveform.	10
3.	(a)	Explain with block diagram the armsture control method for speed control of a DC shunt motor.	10
	(b)	What is the difference between Natural and Forced commutation? Explain any two forced commutation techniques of SCR with waveforms.	10
4.	(a)	Explain any two circuits using SCR for the speed control of single phase induction motor.	10
	(b)	Draw the circuit diagram and explain the working of Jones chopper	10
5.	(a)	Discuss the thyristerised over load and over voltage protection of DC Motor.	10
	(b)	Explain important characteristics of OPAMP	05
	(c)	Draw a neat schematic diagram for OP-AMP in inverting and non-inverting mode	0.5
6.	(a)	Which gates are called as Universal gates? Why?	10
		Using K-map reduce following Boolean function and implement it using NAND gates	
		$f(A,B,C)=\sum m(0,1,3,4,5,6)$	
	(b)	Explain the various interepts of 8085	0.5

(a) Explain the architecture of 8085 Microprocessor with block diagram.

(b) Explain 555 timer as a stable multivibrator

(c) Explain DeMorgan's theorem.

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Table of Contents Industrial Electronics Technology Question Paper

- 1. Understanding the eBook Industrial Electronics Technology Question Paper
 - The Rise of Digital Reading Industrial Electronics Technology Question Paper
 - Advantages of eBooks Over Traditional Books
- 2. Identifying Industrial Electronics Technology Question Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Industrial Electronics Technology Question Paper
 - User-Friendly Interface
- 4. Exploring eBook Recommendations from Industrial Electronics Technology Question Paper
 - Personalized Recommendations
 - Industrial Electronics Technology Question Paper User Reviews and Ratings
 - Industrial Electronics Technology Question Paper and Bestseller Lists
- 5. Accessing Industrial Electronics Technology Question Paper Free and Paid eBooks

- Industrial Electronics Technology Question Paper Public Domain eBooks
- Industrial Electronics Technology Question Paper eBook Subscription Services
- Industrial Electronics Technology Question Paper Budget-Friendly Options
- 6. Navigating Industrial Electronics Technology Question Paper eBook Formats
 - o ePub, PDF, MOBI, and More
 - Industrial Electronics Technology Question Paper Compatibility with Devices
 - Industrial Electronics Technology Question Paper Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Industrial Electronics Technology Question Paper
 - Highlighting and Note-Taking Industrial Electronics Technology Question Paper
 - Interactive Elements Industrial Electronics Technology Question Paper
- 8. Staying Engaged with Industrial Electronics Technology Question Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Industrial Electronics Technology Question Paper
- 9. Balancing eBooks and Physical Books Industrial Electronics Technology Question Paper
 - Benefits of a Digital Library
 - o Creating a Diverse Reading Collection Industrial Electronics Technology Question Paper
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Industrial Electronics Technology Question Paper
 - Setting Reading Goals Industrial Electronics Technology Question Paper
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Industrial Electronics Technology Question Paper
 - Fact-Checking eBook Content of Industrial Electronics Technology Question Paper
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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