FLASHWAVE* 4500 Multiservice Provisioning Platform

Features and specifications

Synchronization

Architectures	Terminal Linear ADM (1+1) Unideectional Path Switched Ring (UPSR) Two Fiber-Bidirectional Line Switched Ring (2F-BLSR) Four Fiber-Bidirectional Line Switched Ring (4F-BLSR) Dedicated Path Protected Mesh (DPPM) Resilient Packet Ring (RPR)		
Interfaces	051	64-pin AMP* CHAMP connectors	
	DS3/EC1	BNC connectors	
	OC-3/STM-1	SC for dual unit; LC for quad unit, MPO connector for 8-port unit 1310 nm wideband	
	OC-12/STM-4	LC, FC, SC or ST for single unit; LC for quad unit; MPO connector for 8-port unit 1310 nm wideband 1550 nm wideband (single unit only)	
	OC-48/\$TM-16	UC, FC, SC or ST connectors 1310 nm wideband 1550 nm tunable narrowband (8 λ @ 50 GHz)	
	OC-192/STM-64	LC, FC, SC or ST connectors 1310 nm wideband; 1550 nm wideband 1550 nm tunable narrowband (4 \(\lambda\) & 100 GHz) 1550 nm full band tunable to 88 \(\lambda\) (@ 50 GHz spacing) G.709 compliant	
	Ethernet (EoS)	RI-45 connectors for 10/100Base-T Ethernet; LC or SC connectors for Gigabit Ethernet	
	Ethernet Aggregation (trunking port, RPR)	LC connectors for dual port unit 850 nm wideband; 1310 nm wideband High/Low Order Aggregation	

Switching

Maximum Switching Capacity					
Specification	Single MSPP	Main Shelf MSSPIMSPP	Double Shelf MSSP	Triple Shelf MSSP	
Switch Capacity	70 Gbps	140 Gbps	220 Gbps	300 Gbps	
STS-1 Cross- Connects	1344 x 1344	2688 × 2688* 5760 × 5760**	5760 x 5760	5760 x 5760	
VT1.5 Cross- Connects (10 Gbps)		21,504 x 21,504 (40 Gbps)	21,504 x 21,504 (40 Gbps)	21,504 x 21,504 (40 Gbps)	

	Requires.	140	Gbps switch	Fabric
and the same	Section 1975 and the second	The section is	William Co., and the Co., and t	March Street

Protection	DS1	1+1 or unprotected	
	DS3/EC1	1:n (n=1 to 6) or unprotected	
	DS3 Transmux	1 = 1 or 1:n (m=1 to 6)	
	OC-3/STM-1	1+1, UPSR, DPPM or unprotected	
	OC-12/58M-4	1+1, UPSR, DPPM or unprotected	
	OC-48/5TM-16	T=1, UPSR, 2F-BLSR, DPPW or unprotected	
	OC-192/STM-64	1+1, UPSR, 2F-BLSR, 4F-BLSR, DPPM or unprotected	
	Ethernet	RPR or unprotected Port Link Aggregation or Unit Link Aggregation	
	Switch matrices	1 = 1	
	Synchronization	1-1	

 Synchronization status messaging (som)
 D51 Building Integrated Timing Supply (BITS) primary
and secondary clock output/input
Line timing

· Internal Stratum 3 timing source

Maximum Numbe	s of Unprote	cted (Protected) :	Service Intest.	ne ers
Туре	Ports/ Card	Single Shelf MSPR/MSSP	Double Shelif MSSP	Triple Shelf MSSP
DS1 (Integrated)	2.8	84 (84)	168 (168)	252 (252)
DS1 (High-Density)	168	6720	13,440	20,160
DS3/EC1	8	96 (96)	192 (192)	288 (288)
DS3 Transmux	1.2	144	288	432
OC-3/3c/STM-1	8	160 (80)	320 (160)	480 (240)
OC-12/12o/STM-4	8	160 (80)	320 (160)	480 (240)
OC-48/48c/STM-16	2	40 (20)	80 (40)	120 (60)
OC-192/192u/STM-64	1	10 (5)	20 (10)	30 (15)
10r100Base-T Ethernet	4 (8)	80	160	240
Gigabit Ethernet	2	40	80	120

Flashwave 4500 Manual

Minjie Lin

Flashwave 4500 Manual:

Whispering the Strategies of Language: An Psychological Journey through Flashwave 4500 Manual

In a digitally-driven earth wherever displays reign supreme and instant conversation drowns out the subtleties of language, the profound strategies and emotional subtleties concealed within words usually move unheard. However, nestled within the pages of **Flashwave 4500 Manual** a captivating literary treasure blinking with natural feelings, lies a fantastic quest waiting to be undertaken. Published by an experienced wordsmith, that marvelous opus attracts viewers on an introspective trip, softly unraveling the veiled truths and profound influence resonating within the material of each word. Within the mental depths with this poignant review, we shall embark upon a genuine exploration of the book is primary themes, dissect its fascinating publishing model, and fail to the effective resonance it evokes serious within the recesses of readers hearts.

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Flashwave 4500 Manual Introduction

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