

Lab No. 01

Objective:

- Introduction to MP lab and PIC microcontroller
- Creating a file

Introduction:

In this lab, we will learn how to open the **MP-LAB** software and open the file for programming. Basically, it is the introduction lab for the semester work in Embedded lab. This introduction lab is very useful for coming projects and work in lab.

Theory:

MP-Lab:

MP-lab is a Public domain software and is developed for the embedded applications on PIC and microcontroller and this is developed by multinational company named as Microchip technology. MP lab supports the coding, debugging and programming of microchips, microcontrollers of 8bit, 16bits and DS-PIC microcontroller and also for 32bit software asset management and Pic-Microcontrollers 32 bits.



Figure 1.1 MP-Lab main screen

PIC-Microcontroller:

The word PIC stands for **Peripheral Interface Controller**. Firstly, PIC developed for the PDP **Program Data processor** computers for controlling the Peripheral devices. These are very easy

LAB instructor:

Engr. Syed Muhammad Umar
UET Lahore FSD Campus

Embedded Systems Lab Manual Objective

Thomas Griffiths



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Real-Time Embedded Systems Xiacong Fan, 2015-02-25 This book integrates new ideas and topics from real time systems embedded systems and software engineering to give a complete picture of the whole process of developing software for real time embedded applications You will not only gain a thorough understanding of concepts related to microprocessors interrupts and system boot process appreciating the importance of real time modeling and scheduling but you will also learn software engineering practices such as model documentation model analysis design patterns and standard conformance This book is split into four parts to help you learn the key concept of embedded systems Part one introduces the development process and includes two chapters on microprocessors and interrupts fundamental topics for software engineers Part two is dedicated to modeling techniques for real time systems Part three looks at the design of software architectures and Part four covers software implementations with a focus on POSIX compliant operating systems With this book you will learn The pros and cons of different architectures for embedded systems POSIX real time extensions and how to develop POSIX compliant real time applications How to use real time UML to document system designs with timing constraints The challenges and concepts related to cross development Multitasking design and inter task communication techniques shared memory objects message queues pipes signals How to use kernel objects e g Semaphores Mutex Condition variables to address resource sharing issues in RTOS applications The philosophy underpinning the notion of resource manager and how to implement a virtual file system using a resource manager The key principles of real time scheduling and several key algorithms Coverage of the latest UML standard UML 2.4 Over 20 design patterns which represent the best practices for reuse in a wide range of real time embedded systems Example codes which have been tested in QNX a real time operating system widely adopted in industry

Applied Biomechanics Lab Manual John C. Garner, Charles Allen, Harish Chander, Adam C. Knight, 2022-07-12 Applied Biomechanics Laboratory Manual With HKPropel Online Video provides guided opportunities for students to connect their conceptual understanding of biomechanics to practical applications As readers progress through 13 easy to follow experiential based learning labs they will gain insight into how these mechanical principles relate to areas such as sport performance athletic injury ergonomics and rehabilitation This manual engages students with full color images as well as visual aids It is an ideal primary or supplemental text for any biomechanics and kinesiology curriculum Applied Biomechanics Laboratory Manual comprises 13 laboratory chapters that offer more than 30 lab activities Each laboratory chapter provides at least one complete lesson including objectives key terms and introductory content that set the stage for learning Each lab activity is broken down into step by step procedures providing guidance for those new to lab settings so that they may complete the process with confidence Related online learning tools delivered through HKPropel include digital versions of the forms found in the book as well as online video clips that simulate the experience of performing many of the lab activities The text is organized in a logical progression that builds on the knowledge students acquire as they advance Written by

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UML-B Specification for Proven Embedded Systems Design Jean Mermet,2013-03-19 This book presents the perspective of the project on a Paradigm Unifying System Specification Environments for proven Electronic design PUS SEE as conceived in the course of the research during 2002 2003 The initial statement of the research was formulated as follows The objective of PUSSEE is to introduce the formal proof of system properties throughout a modular system design methodology that integrates sub systems co verification with system refinement and reusability of virtual system components This will be done by combining the UML and B languages to allow the verification of system specifications through the composition of proven sub systems in particular interfaces using the VSIAISLIF standard The link of B with C VHDL and SystemC will extend the correct by construction design process to lower system on chip SoC development stages Prototype tools will be developed for the code generation from UML and B and existing B verification tools will be extended to support IP reuse according to the VSI Alliance work The methodology and tools will be validated through the development of three industrial applications a wireless mobile terminal a telecom system on chip based on HIPERLAN2 protocol and an anti collision module for automobiles The problem was known to be hard and the scope ambitious But the seventeen chapters that follow describing the main results obtained demonstrate the success of the research acknowledged by the European reviewers They are released to allow the largest audience to learn and take benefit of *Embedded Systems LAB Manual* L Malathi,2021-05-06 The Embedded Systems Laboratory Manual is having the laboratory experiments related to embedded systems It will be useful for Electronics and Communication Engineering also other engineering program and courses The core design of this book concentrated for laboratory aspect All the modules includes Analysis Discussion and Conclusion part Which will give the good practical knowledge to the students in different observations of the individual experiment Many interfacing concepts have dealt for embedded systems like ADC DAC LCD LED and PWM Stepper Motor Temperature Sensor Keyboard EPROM and Interrupt Real Time Clock and Serial Port **Part - Anatomy & Physiology Laboratory Manual - E-Book** Kevin T Patton,

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Communications, Signal Processing, and Systems Qilian Liang, Wei Wang, Xin Liu, Zhenyu Na, Min Jia, Baoju Zhang, 2020-04-04 This book brings together papers from the 2019 International Conference on Communications Signal Processing and Systems which was held in Urumqi China on July 20-22 2019 Presenting the latest developments and discussing the interactions and links between these multidisciplinary fields the book spans topics ranging from communications to signal processing and systems It is chiefly intended for undergraduate and graduate students in electrical engineering computer science and mathematics researchers and engineers from academia and industry as well as government employees

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