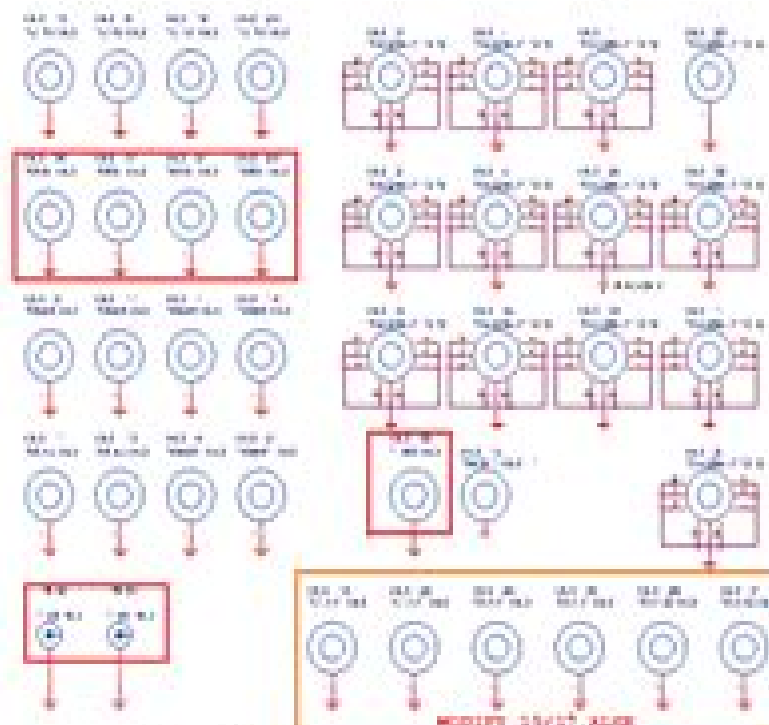




# PROCESSOR HYPERTRANSPORT INTERFACE

OUT-OF-BOARD USE FOR CONNECTIONS TO A LOCAL PERIPHERAL  
SEE THE BOARD'S USER MANUAL FOR ADDITIONAL INFORMATION

HT0 & HT1



MODIFY CPU/DT, ADDR



LAYOUT: Place bypass cap on top side of board  
INSTALL AT LEAST ONE 100nF BYPASS CAP ON EACH SIDE OF THE BOARD  
DO NOT CONNECT TO ANY OTHER BOARD  
DO NOT CONNECT TO ANY OTHER BOARD



Quantia Computer Inc.

PRODUCT : 00100

ASD-00100 HT 10

# Emachines G420 Manual

**Rosina Ehmann**



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