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yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck *NASA Contractor Report* ,1981 *The 14th IEEE 2003 International Symposium on Personal, Indoor, and Mobile Radio Communications* Ke Gong,Zhisheng Niu,2003

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