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## First/Second Semester B.E. Degree(CBCS)Examination

## Basic Electronics

Time: 3 hrs.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing one full question from each module.

Module-1

1.
  - a. Define following diode parameters:
    - i) PIV
    - ii) Knee voltage
    - iii) Dynamic resistance. (06 Marks)
  - b. With neat circuit diagram and waveforms, explain the working of a half wave rectifier. (06 Marks)
  - c. Draw common emitter circuit and sketch the input and output characteristics. Also explain operating regions by indicating them on characteristics curve. (04 Marks)

## OR

2.
  - a. Explain zener diode voltage regulator circuit with no load and with load. (06 Marks)
  - b. The i/p to the Full wave rectifier is  $v(t) = 200 \sin 50t$ . If  $R_L$  is  $1k\Omega$  and forward resistance of diode is  $50\Omega$ , find:
    - i) D.C current through the circuit
    - ii) The A.C (rms) value of current through the circuit
    - iii) The D.C output voltage
    - iv) The A.C power input
    - v) The D.C power output
    - vi) Rectifier efficiency. (06 Marks)
  - c. Derive the relationship between  $\alpha$  and  $\beta$ . Calculate the value of  $I_c$ ,  $I_e$  for a transistor that has  $\alpha = 0.98$  and  $I_b = 100\mu A$ . (04 Marks)

Module-2

3.
  - a. What is a Dc load line? Explain Base biased method with necessary equations. (05 Marks)
  - b. Explain the characteristics of an Ideal Op-amp. Mention two applications of Op-Amp. (06 Marks)
  - c. With a neat circuit diagram, explain the Voltage Divider Bias circuit using approximate analysis. (05 Marks)

## OR

4.
  - a. Design the voltage divider bias circuit to operate from 12 V supply. The bias conditions are  $V_{CE}=3V$ ,  $V_E=5V$ , and  $I_c = 1mA$ . (05 Marks)
  - b. Derive the expression of 3 input summing amplifier. (05 Marks)
  - c. Explain voltage follower with neat circuit and necessary equations. (06 Marks)

Module-3

5.
  - a. Convert  $(1101101)_2 = ( \quad )_{10}$  and  $(69)_{10} = ( \quad )_2$ . (04 Marks)
  - b. Convert  $(1010111011110101)_2 = ( \quad )_{10}$  and  $(FA876)_{16} = ( \quad )_2$ . (04 Marks)
  - c. State and prove Demorgan's Theorem. (08 Marks)

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**RS Peters**



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