



basic education

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GRADE 11

MATHEMATICS P1

EXEMPLAR 2013

MEMORANDUM

MARKS: 150

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web for sample syntax and a list of vhdل statements supported by the vhdل synthesizer see appendix a quick reference for a list of exceptions and constraints on the vhdل synthesizer s support of vhdل see appendix b limitations this chapter shows you the structure of a vhdل design and then

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bit and gate how do i achieve this in vhdل note i am trying to use re usable vhdل code so i want to avoid hard coding something like result array 0 and array 1 and array 2 and array n thanks oshara

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